5200D RX

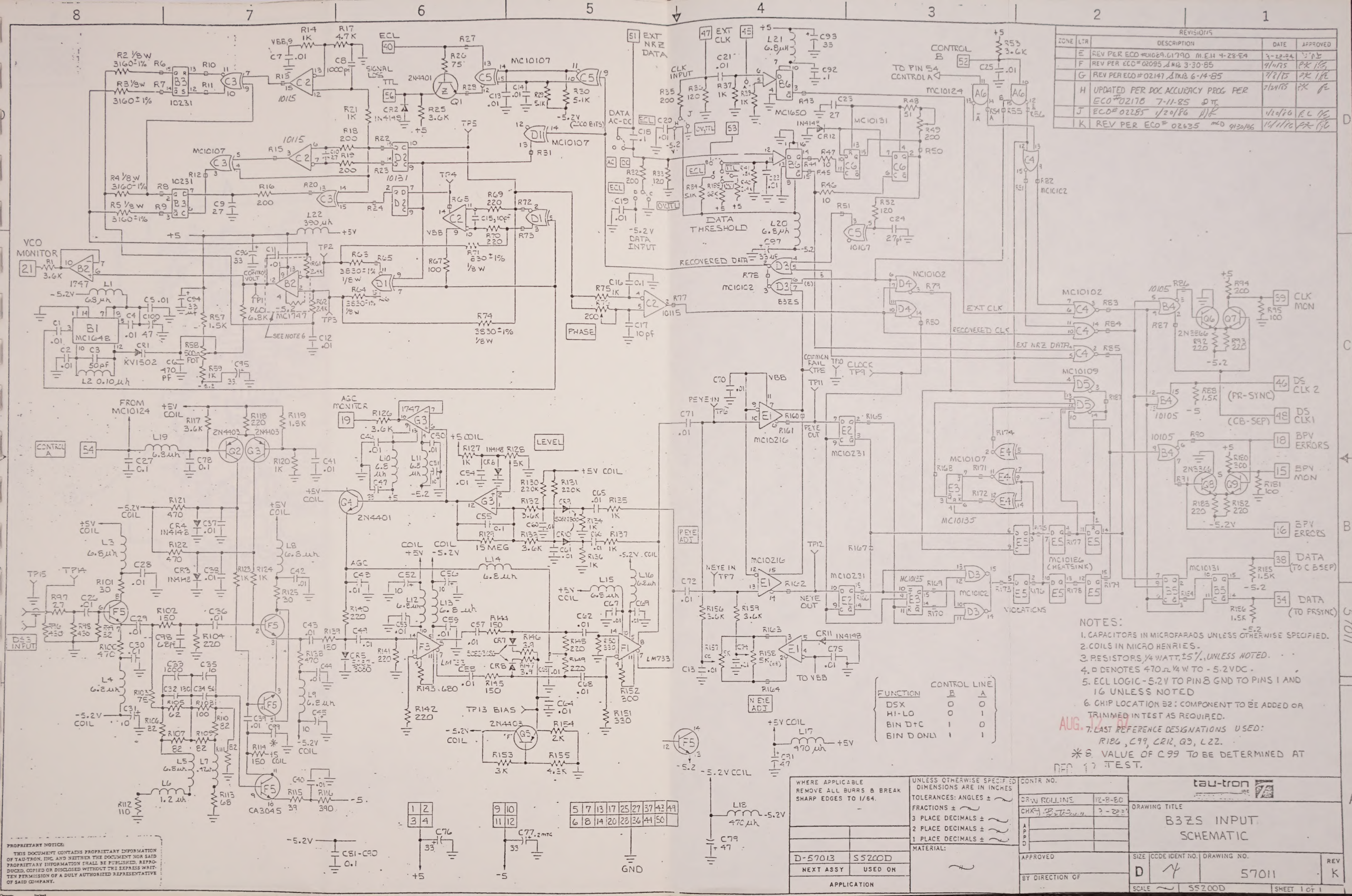
SCHEMATICS

tau-tron

A UNIT OF
GENERAL SIGNAL

5200D RX

SCHEMATICS



REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	E	REV PER ECO #01029.01790 M.E.H 4-28-84	4-28-84	[illegible]
	F	REV PER ECO # 02095 SMB 3-30-85	4/11/85	PK [illegible]
	G	REV PER ECO # 02147 SMB 6-14-85	7/8/85	PK [illegible]
	H	UPDATED PER DOC ACCURACY PROG PER ECO # 02178 7-11-85 DTT	7/24/85	PK [illegible]
	J	ECO # 02285 1/20/86 [illegible]	1/20/86	[illegible]
	K	REV PER ECO # 02635 MCD 9/30/86	10/1/86	PK [illegible]

NOTES:

1. CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. COILS IN MICRO HENRIES.
3. RESISTORS 1/4 WATT, ±5%, UNLESS NOTED.
4. □ DENOTES 470Ω 1/4 W TO -5.2VDC.
5. ECL LOGIC -5.2V TO PIN 8 GND TO PINS 1 AND 16 UNLESS NOTED
6. CHIP LOCATION B2: COMPONENT TO BE ADDED OR TRIMMED IN TEST AS REQUIRED.
7. LAST REFERENCE DESIGNATIONS USED: R186, C99, CR12, Q9, L22.
8. VALUE OF C99 TO BE DETERMINED AT TEST.

AUG. 12 '87

FUNCTION	CONTROL LINE B	CONTROL LINE A
DSX	0	0
HI-LO	0	1
BIN D+C	1	0
BIN D ONLY	1	1

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

D-57013	S5200D
NEXT ASSY	USED ON
APPLICATION	

CONTR NO.
DR W ROLLINS 12-8-80
CHK [illegible] 9-23-[illegible]
APPD
APPROVED
BY DIRECTION OF

tau-tron inc

DRAWING TITLE
B3ZS INPUT SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	4	57011	K

SCALE ~ S5200D SHEET 1 OF 1

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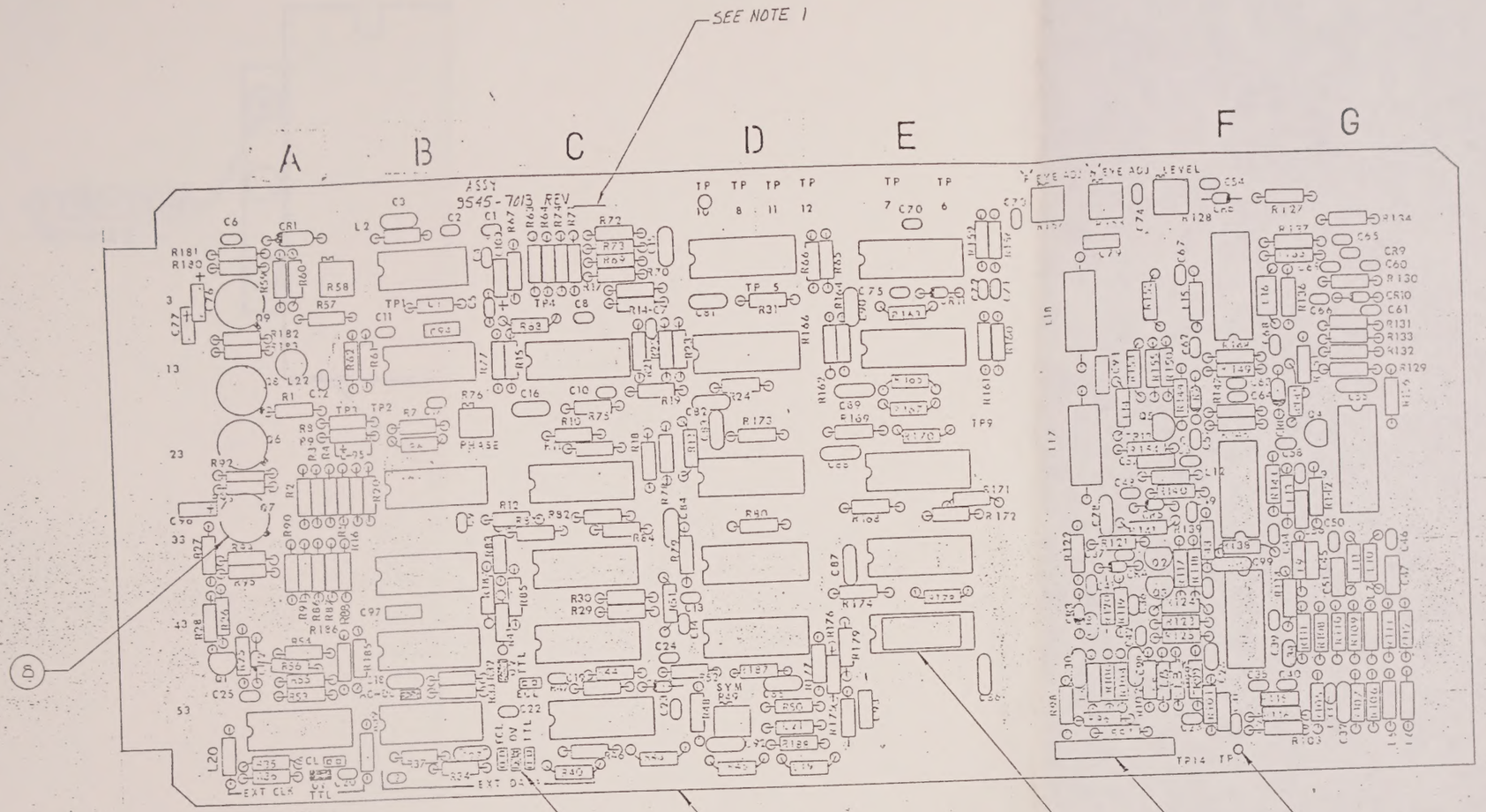
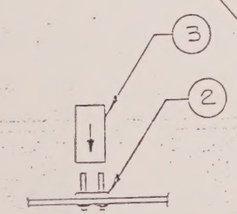
K	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO # 021 78 7-3-85 DH	7/23/85	PR	
L	REV PER ECO # 02208 TMM 8-15-85	9/12/85	RL	
M	REV PER ECO # 02262 TMM 10/9/85	11/5/85	-K	
N	REV PER ECO # 02263 TMM 11/26/85	1/20/86	KL	





PLACE JUMPERS OVER PINS 4 PLACES AS SHOWN BY

FIGURE 1

NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. INSTALL SCREWS FROM TOP SIDE.

AUG. 12. '87

D-57626	55250
E-57158	55200E RX
D-57350	55200D Rx
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL:

DR	MOWRY	11-20-80
CHK	B. COOPER	12-8-80
	W. ROLLINS	12-8-80

APPROVED

tau-tron

DRAWING TITLE: B3 ZS INPUT PCB ASSEMBLY

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	954	57013	N

SCALE 2/1 | 5200D RX | SHEET 1 OF 2

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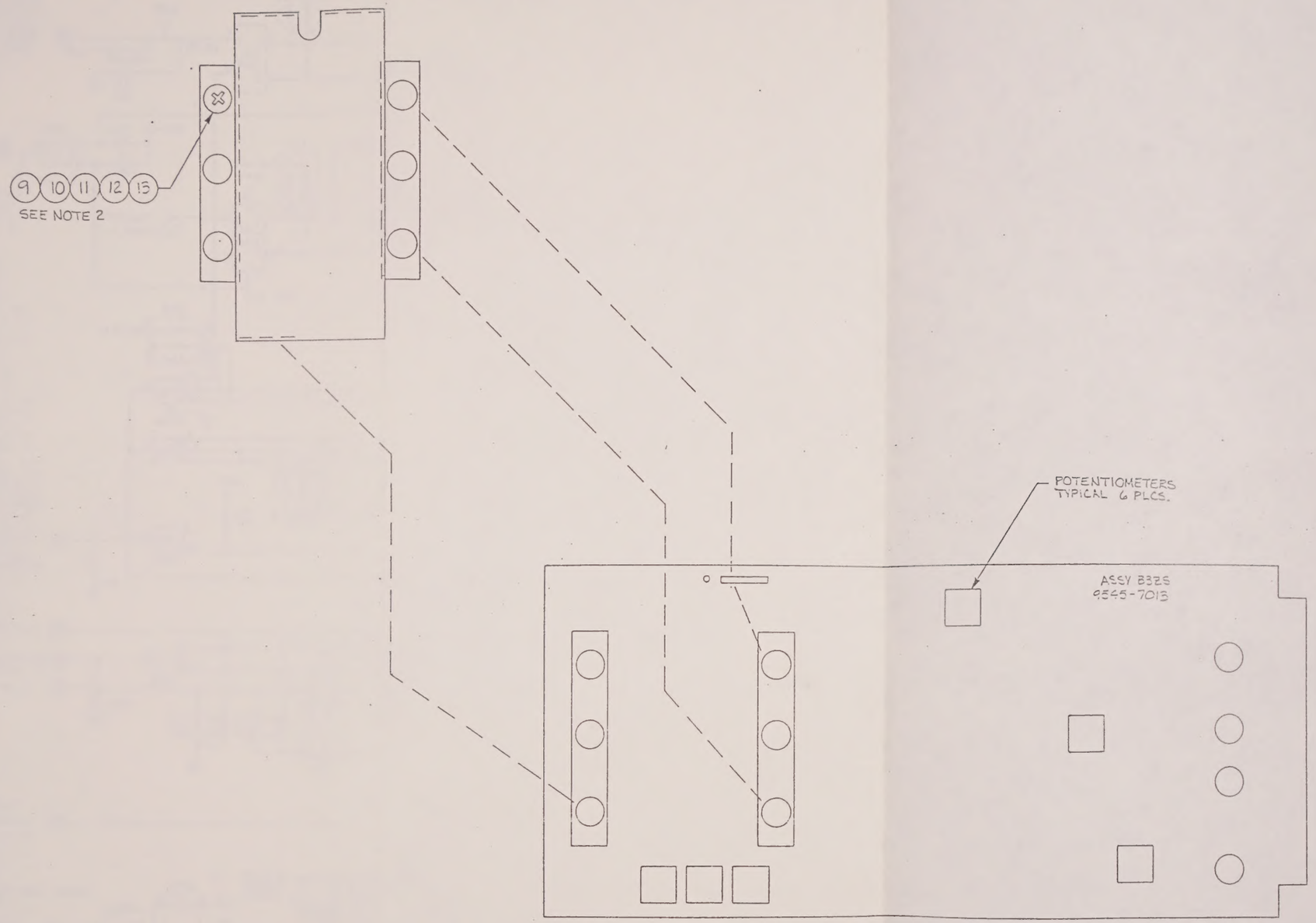
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
SEE SHEET ONE FOR REVISIONS				



AUG. 12. '87

WHERE APPLICABLE. REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL:	CONTR NO. DR T. MURPHY 8-15-85 CHK APPD APPROVED E[illegible]RECTION OF	tau-tron A UNIT OF GENERAL SIGNAL DRAWING TITLE B3ZS INPUT PCB ASSEMBLY
D-57626 S5250 E-57158 S5200E RX D-57350 S5200D RX NEXT ASSY / USED ON APPLICATION			SIZE D · CODE IDENT NO. · DRAWING NO. 57013 · REV N SCALE 2/1 · 5200D RX · SHEET 2 OF 2

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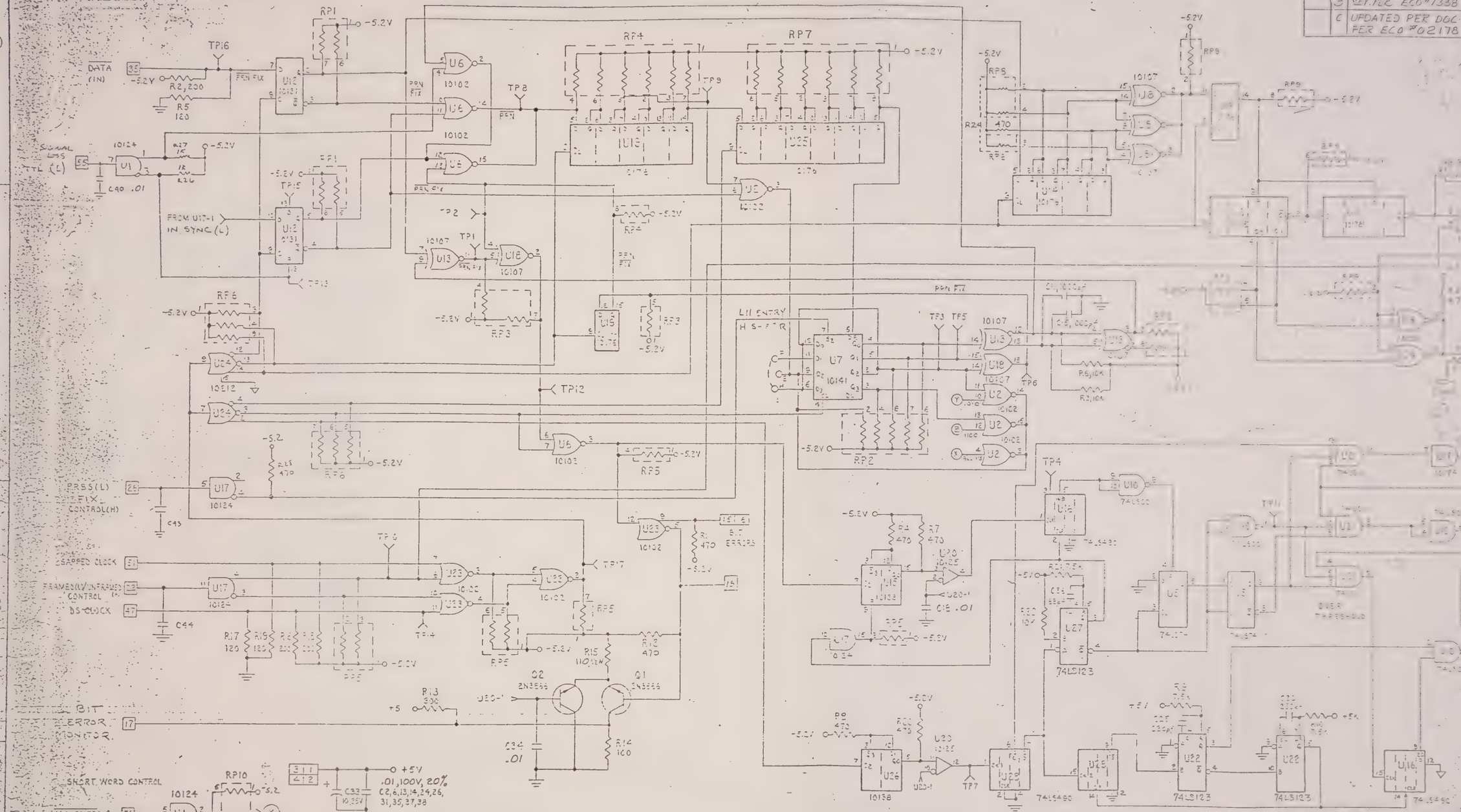
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	3	REVISED AND REDRAWN CS 4-10-80		
	4	REV PER PDC, 00745 MRC	10-14-80	
	A	REV & RELEASE PER DCN 00793	1-27-81	
	B	REV. PER ECO #1338 25FEB83	2-25-83	
	C	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-8-85 DH	7/23/85	



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. RESISTANCE VALUES ARE IN OHMS, 1/4 W, 5%.
B. CAPACITANCE VALUES ARE IN MICROFARADS.
C. RESISTOR PACKS ARE 470 Ω.
2. UNMARKED CAPACITORS ARE 0.1 CERAMIC.
3. X TO BE DETERMINED IN TEST.

REFERENCE DRAWINGS:
ASSEMBLY: TDK-57016
FABRICATION: TCS-57015

LAST REF DES USED	UNUSED
U25	
RP10	
R28	R1
C44	
Q2	
TP17	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

TDK-57016	552000 FX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR 4-10-80
CHK 4-11-80
APPD
APPROVED:
BY DIRECTION OF

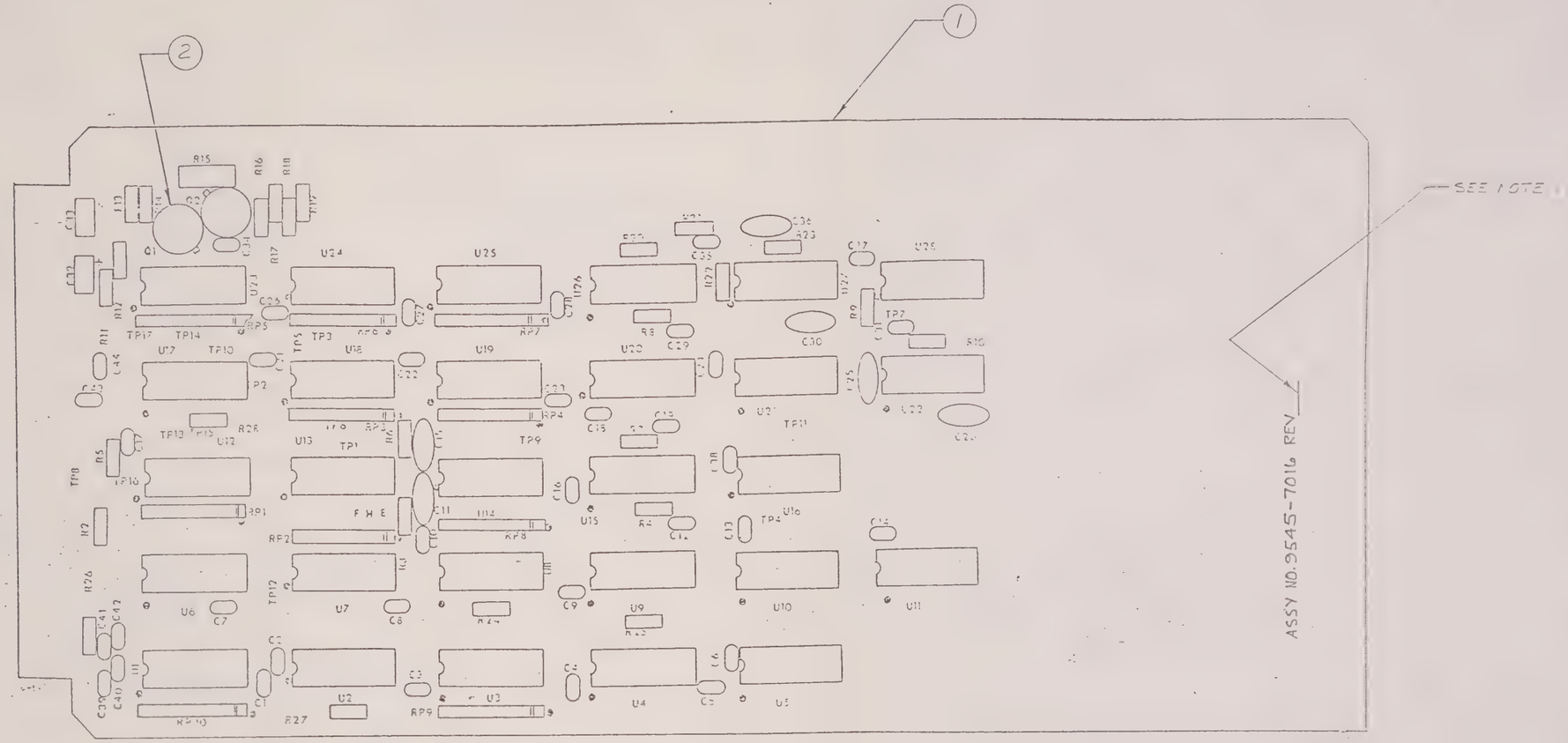
tau-tron INC
Chelmsford, Ma. 01824

DRAWING TITLE
SCHEMATIC
P.R. SYNC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57014	C

SCALE 1 | 552000 FX | SHEET 1 OF 1

D	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO 02178 7-1-85	7/23/85	



NOTES:

1. PLACE A PERMANANT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES = ~ FRACTIONS = ~ 3 PLACE DECIMALS = ~ 2 PLACE DECIMALS = ~ 1 PLACE DECIMALS = ~	DR. C.A. CHK. B.C. K.B.	tau-tron ASSEMBLY P.R. SYNC P.C. BD.
TER-57158 | S52000 RX NEXT ASSY | USED ON APPLICATION	MATERIAL: ~	APPROVED BY DIRECTION OF	SIZE D | CODE IDENT NO 954 | DRAWING NO 57016 | REV D SCALE 2/1 | S52000 RX | SHEET 1 OF 1

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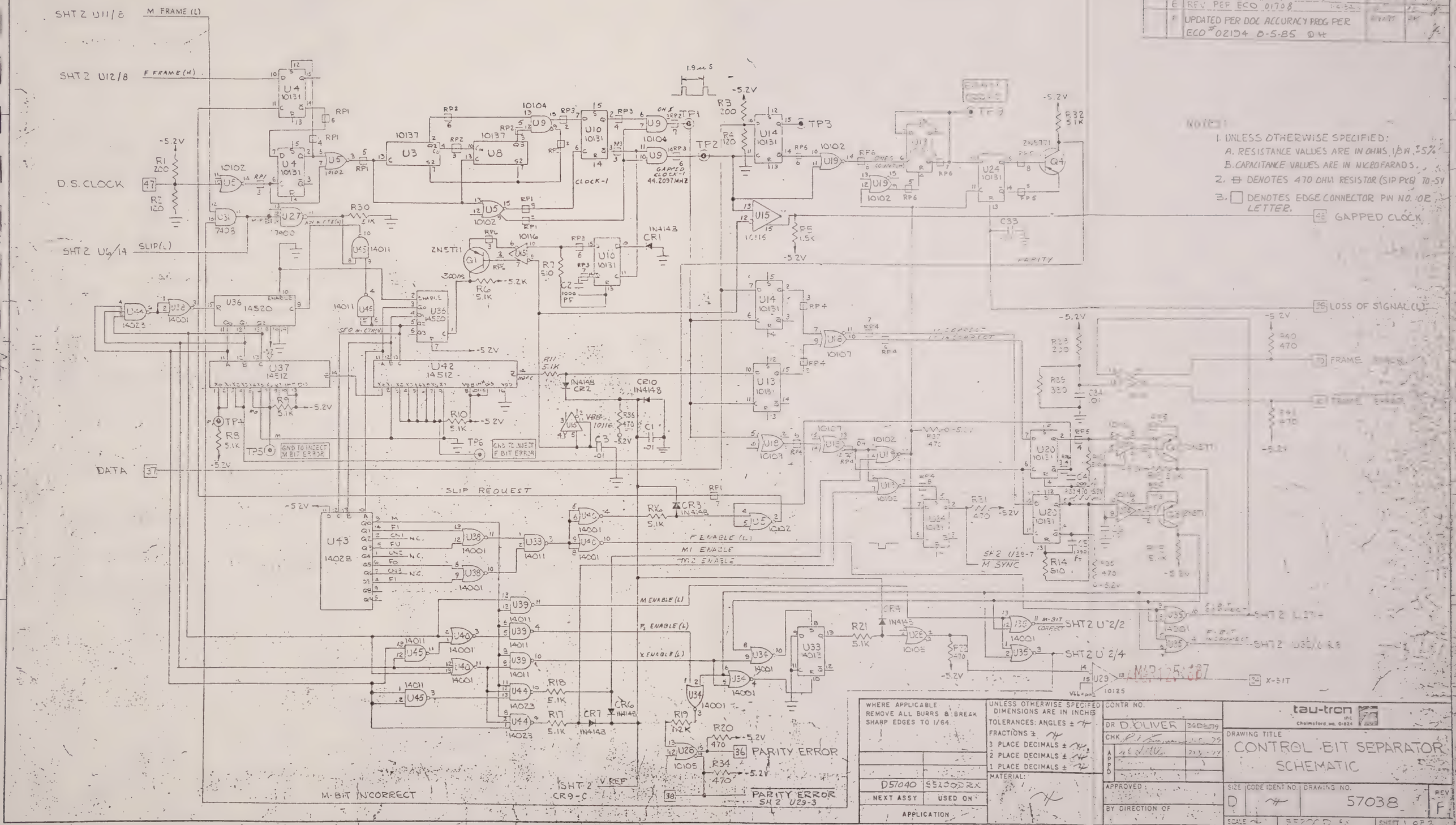
LINE	LTR	DESCRIPTION	DATE	APPROVED
	1	REVISED PER ECO NO 00618	14 FEB 70	
	2	REV PER PDC #00644 CS	3/13/70	
	3	REV PER 00736	5-15-83	
	A	REV PER DCN 00760		
	B	REV PER ECO 00890		
	C	REV PER ECO # 01033 BME 10-23-81		
	D	REV PER ECO 01221		
	E	REV PER ECO 01708		
	F	UPDATED PER DOC ACCURACY PROG PER ECO # 02194 8-5-85 DH		



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, 1/8 W, ±5%
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. ⊟ DENOTES 470 OHM RESISTOR (SIP PKG) TO -5V
3. ☐ DENOTES EDGE CONNECTOR PIN NO. OR LETTER.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

D57040	55200DRX
NEXT ASSY	USED ON

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR NO.
DR D. OLIVER 24 DEC 79
CHK
APPROVED
BY DIRECTION OF

tau-tron INC Chelmsford, MA. 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D		57038	F

SCALE | 55200DRX | SHEET 1 OF 2

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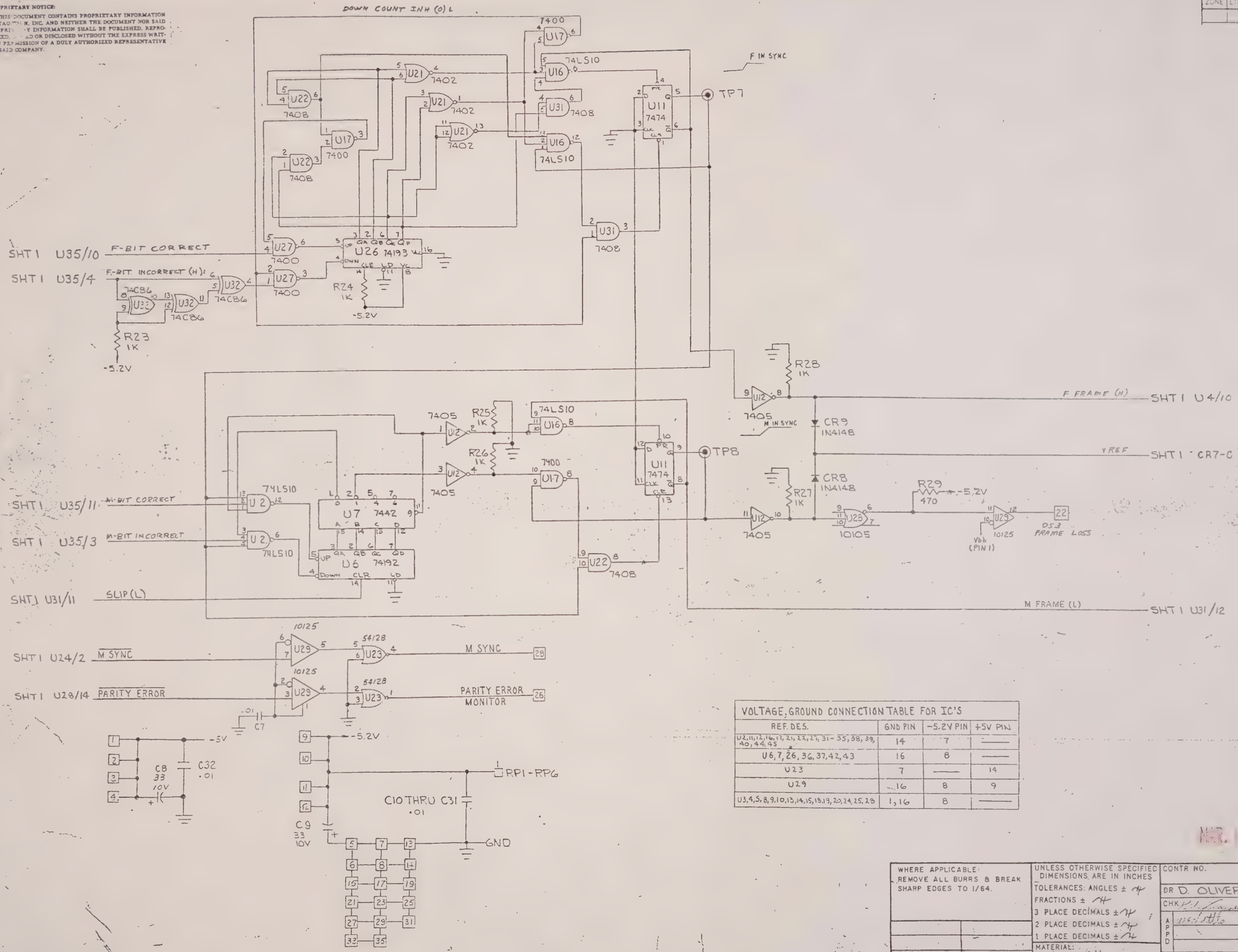
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		



VOLTAGE, GROUND CONNECTION TABLE FOR IC'S			
REF. DES.	GND PIN	-5.2V PIN	+5V PIN
U2,11,12,16,17,21,22,23,31-35,38,39,40,44,45	14	7	—
U6,7,26,36,37,42,43	16	8	—
U23	7	—	14
U29	16	8	9
U3,4,5,8,9,10,13,14,15,18,19,20,24,25,28	1,16	8	—

LAST REF DES USED	UNUSED
U45	U1,30,41
C34	
R41	
Q4	
CR10	CR5
TP9	
RP6	

MAR. 15. '87

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

NEXT ASSY	USED ON
D57D40	S5200DRX

APPLICATION

CONTR NO.
DR D. OLIVER 26DEC79
CHK
APPD
APPROVED
BY DIRECTION OF

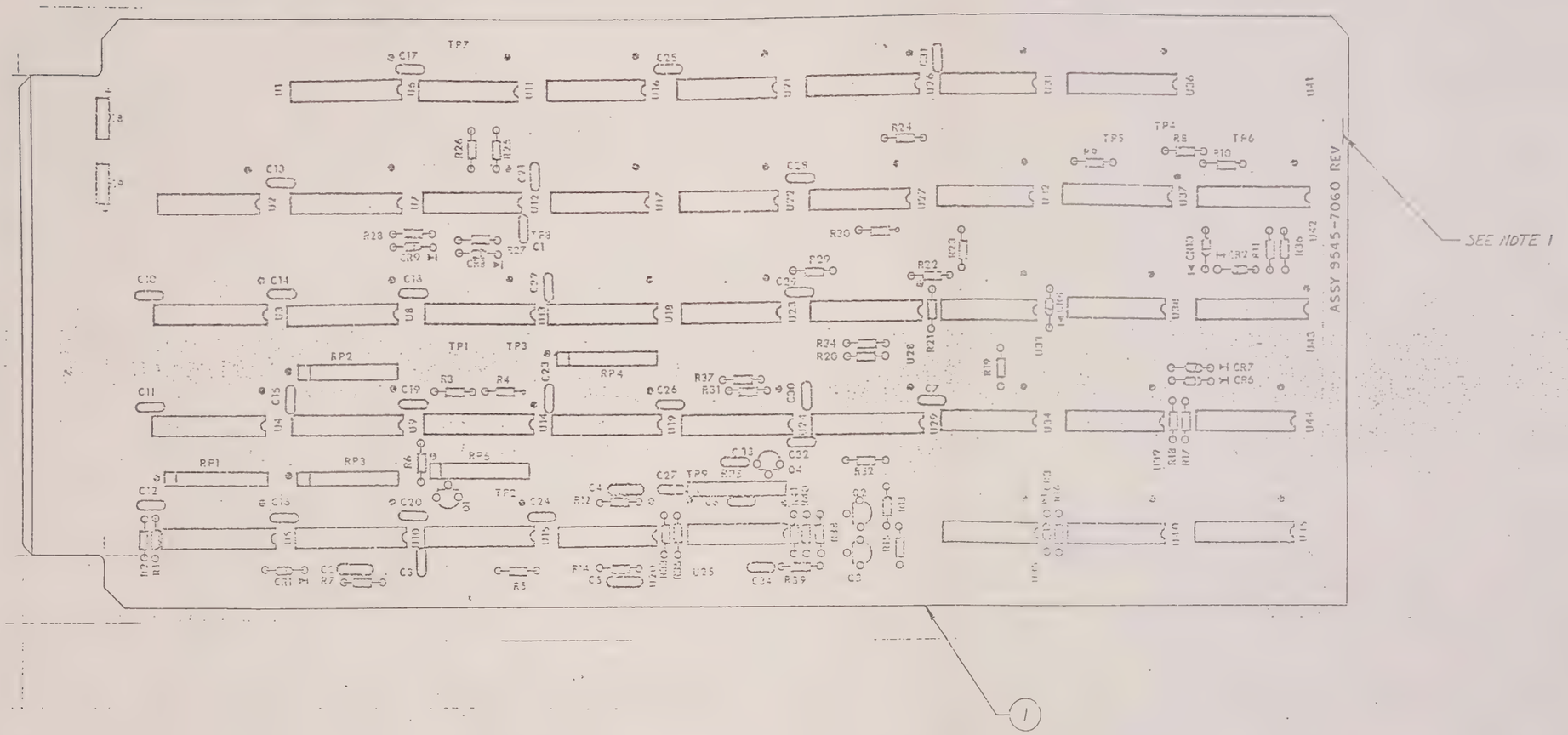
tau-tron INC Chelmsford, Ma. 01824

DRAWING TITLE
CONTROL BIT SEPARATOR SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING-NO.	REV
D	~	57038	F

SCALE ~ | S5200DRX | SHEET 2 OF

	F	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO #02194 7-30-85 DH	8/27/85	PK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± ~ FRACTIONS ± ~ 3 PLACE DECIMALS ± ~ 2 PLACE DECIMALS ± ~ 1 PLACE DECIMALS ± ~	CONTR NO.	tau-tron
		DR. J. HOWARD 8-21-80	DRAWING TITLE: ASSEMBLY CONTROL BIT SEPARATOR PCB
		CHK. G.S. 8-24-80	
S5200D RX	MATERIAL ~	APPROVED	SIZE D · CODE IDENT NO. 954 · DRAWING NO. 57040 · REV F
NEXT ASSY · USED ON		BY DIRECTION OF	SCALE 2:1 · S5200D RX · SHEET 1 OF 1
APPLICATION			

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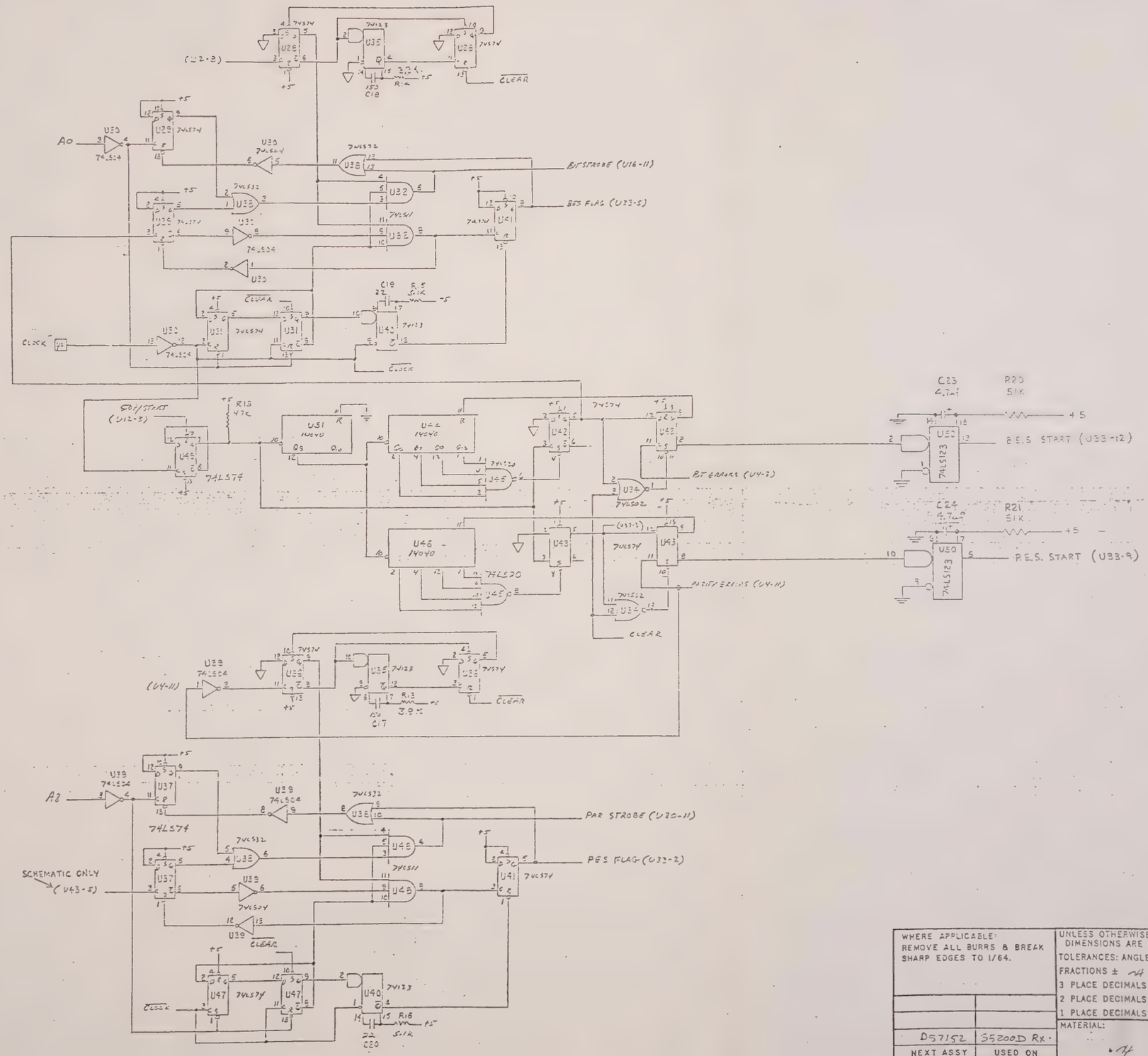
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		



FEB. 6. '87
AUG. 12. '87

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± ⌗ FRACTIONS ± ⌗ 3 PLACE DECIMALS ± ⌗ 2 PLACE DECIMALS ± ⌗ 1 PLACE DECIMALS ± ⌗ MATERIAL: ⌗	CONTR NO. DEC. 12. '86 DR K. BEAUPRE 2/24/80 CHK RAC APPD APPROVED BY DIRECTION OF	tau-tron INC Chelmsford, Ma. 01824 DRAWING TITLE S5200D ERROR COUNTER P.C. BD. SCHEMATIC
D57152 | S5200D Rx NEXT ASSY | USED ON APPLICATION			SIZE D | CODE IDENT NO. ⌗ | DRAWING NO. 57150 | REV F SCALE ⌗ | S5200D Rx | SHEET 2 OF 2

57150

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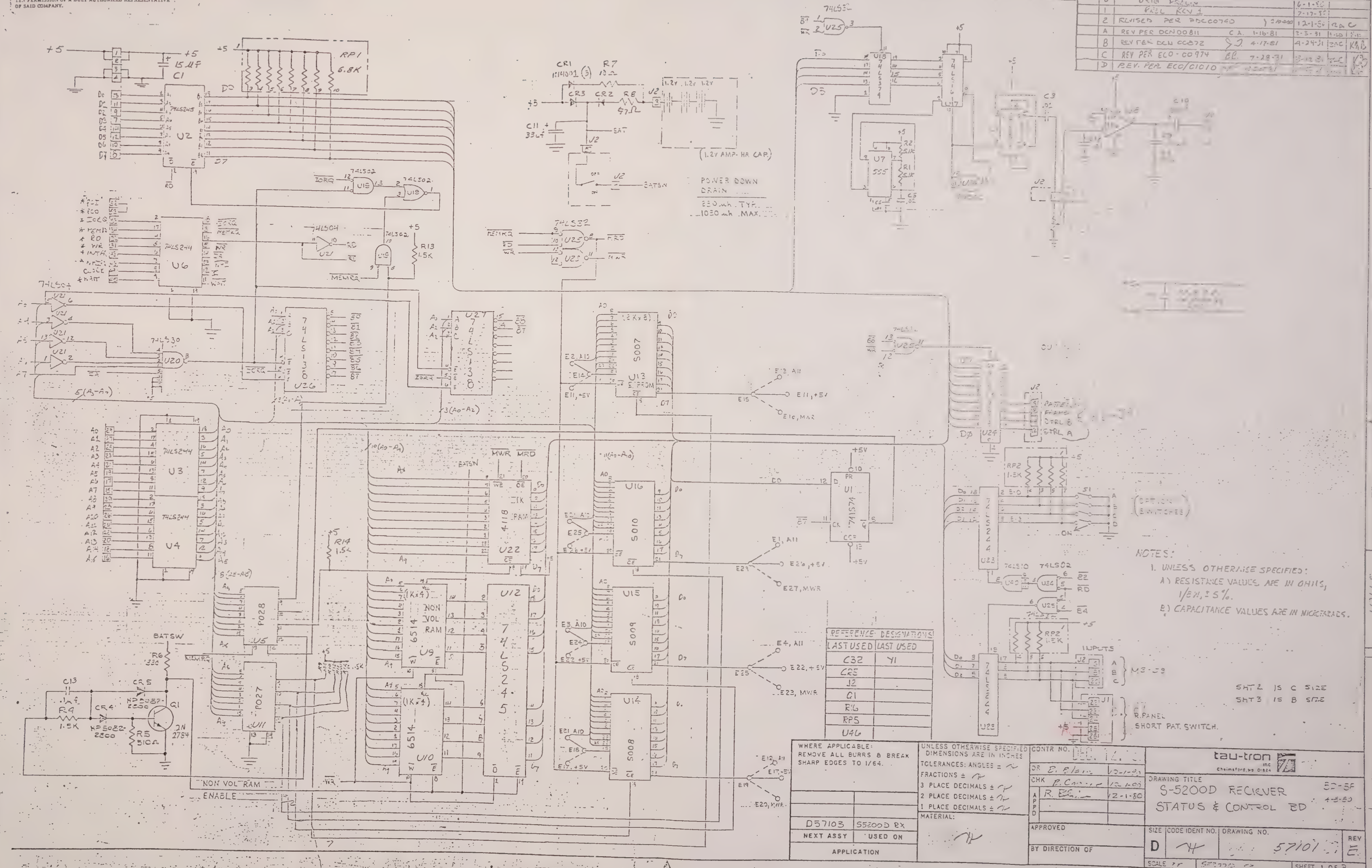
#02194 8-5-85

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	0	ORIG PRELIM	6-1-80	
	1	PREL REV 1	7-17-80	
	2	REVISED PER PDCC0740	12-1-80	RAC
	A	REV PER DCN00811 C.A. 1-16-81	2-3-81	
	B	REV PER DCN 00872 4-17-81	4-24-81	RAC KAB
	C	REV PER ECO-00974 7-28-81	8-12-81	
	D	REV. PER ECO/01010		



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A) RESISTANCE VALUES ARE IN OHMS, 1/8W, ±5%.
   B) CAPACITANCE VALUES ARE IN MICROFARADS.

SHT 2 IS C SIZE
SHT 3 IS B SIZE

REFERENCE DESIGNATIONS	
LAST USED	LAST USED
C32	Y1
C25	
J2	
Q1	
R16	
RP5	
U46	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

NEXT ASSY	USED ON
D57103	S5200D RX

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ⌒
FRACTIONS ± ⌒
3 PLACE DECIMALS ± ⌒
2 PLACE DECIMALS ± ⌒
1 PLACE DECIMALS ± ⌒
MATERIAL: ⌒

CONTR NO. DEC. 14.
DR B. Blain 12-1-80
CHK B. Cooper 12-1-80
APPD R. Blain 12-1-80
APPROVED
BY DIRECTION OF

tau-tron inc Chelmsford, Ma 01824

DRAWING TITLE
S-5200D RECIEVER STATUS & CONTROL BD
5D-5F 4-5-80

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	⌒	57101	E

SCALE ⌒ SHEET 1 OF 3

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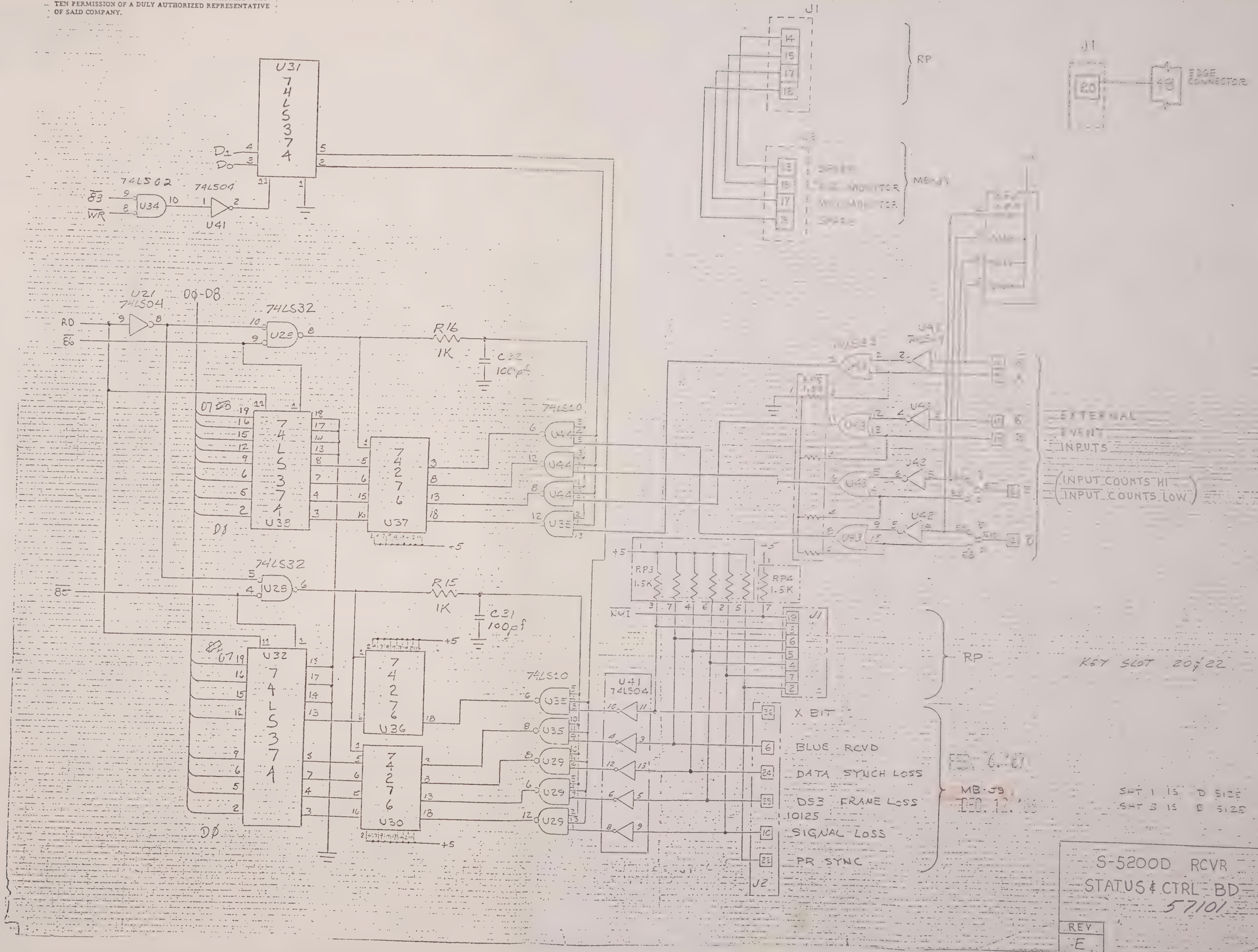

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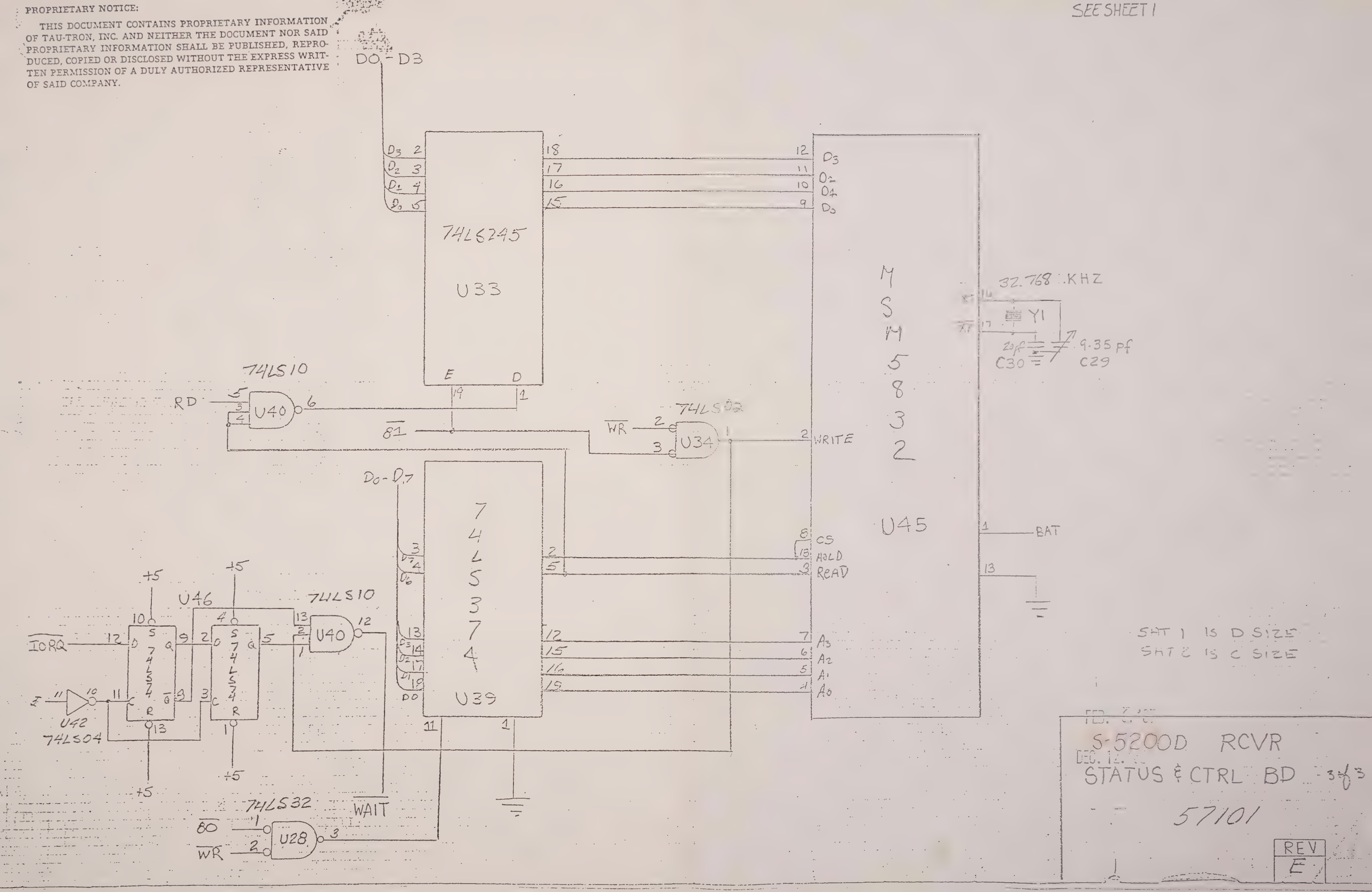
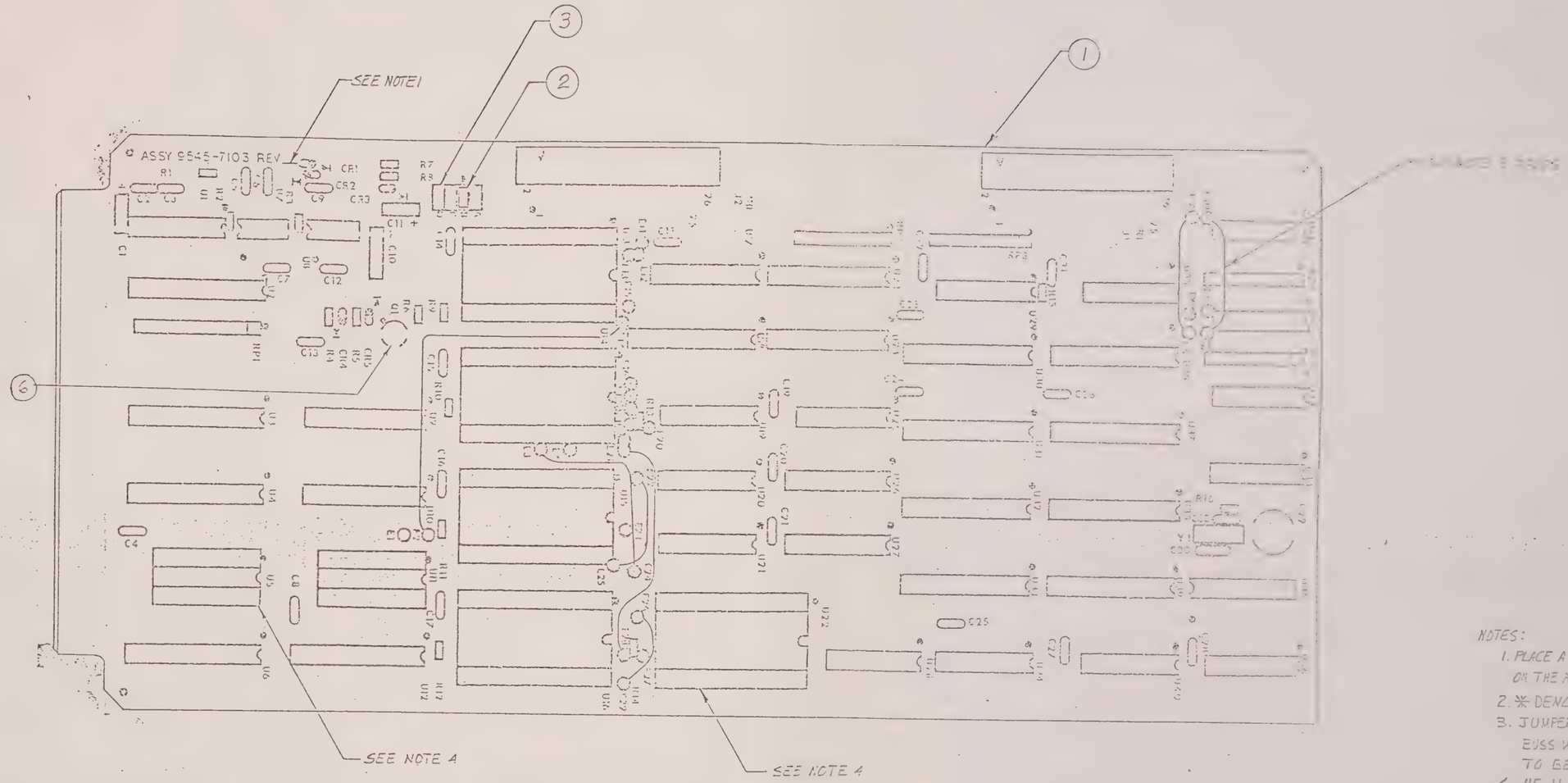
SEE SHEET 1



H	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO#02194 8-14-85 DA	8/2/85	



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. * DENOTES A STATIC SENSITIVE DEVICE.
3. JUMPERS 1 INCH OR LESS TO BE 24 AWG BUSS WIRE WITH SLEEVING. ALL OTHERS TO BE 30 AWG STRANDED WIRE
4. U5, 11, 13 - 16, TO BE INSTALLED IN TEST.

FEB. 6. '85

D57158	S5200D RX		
NEXT ASSY	USED ON		
APPLICATION			

CA	2-2-81
BUDD	2-6-82
K.B	2-6-82

APPROVED

BY DIRECTION OF

tau-tron

ASSEMBLY
STATUS & CONTROL PCB

SIZE	CODE IDENT NO	DRAWING NO	REV
D	954	57103	H

SCALE 2:1 | S5200D RX | SHEET 1 OF 1

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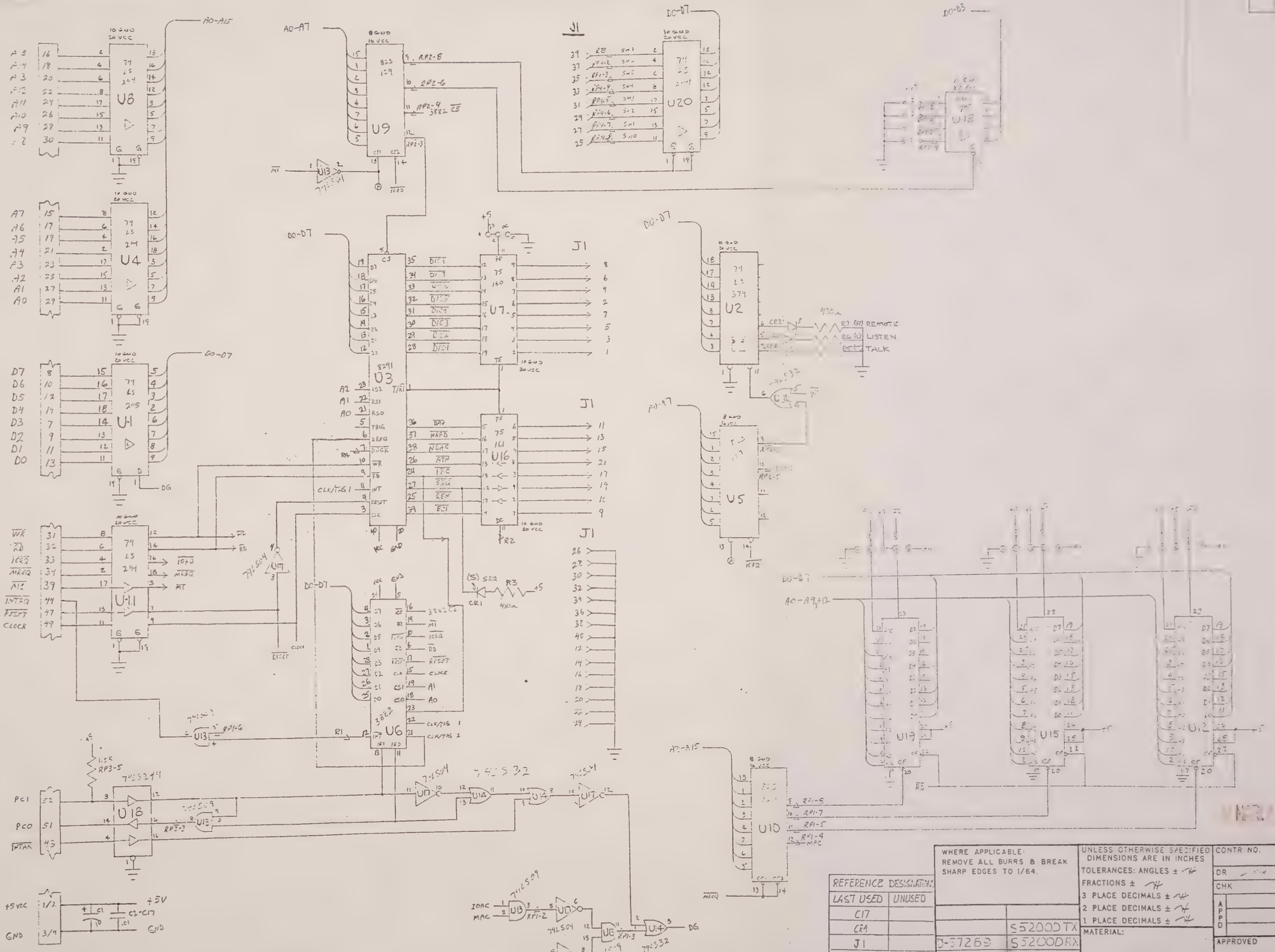
REVISIONS

SYMBOL	DESCRIPTION	DATE	APPROVED
A	RELEASED TO MFG PER ECO 01086	1/8/82	BAC
B	UPDATED PER DOC ACCURACY PROG PER ECO #02194 8-5-85 DH	8/27/85	PK



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, 1/8W, ±5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. ⏚ DENOTES 1.5K TO VCC (+5V).

REFERENCE DESIGNATORS	
LAST USED	UNUSED
C17	
CR4	
J1	
R6	
U20	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

NEXT ASSY	USED ON
D-57269	S5200DTX
	S5200DRX

APPLICATION

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 1/4
FRACTIONS ± 1/4
3 PLACE DECIMALS ± 1/4
2 PLACE DECIMALS ± 1/4
1 PLACE DECIMALS ± 1/4
MATERIAL: N/A

CONTR NO.
DR
CHK
APPD
APPROVED
BY DIRECTION OF

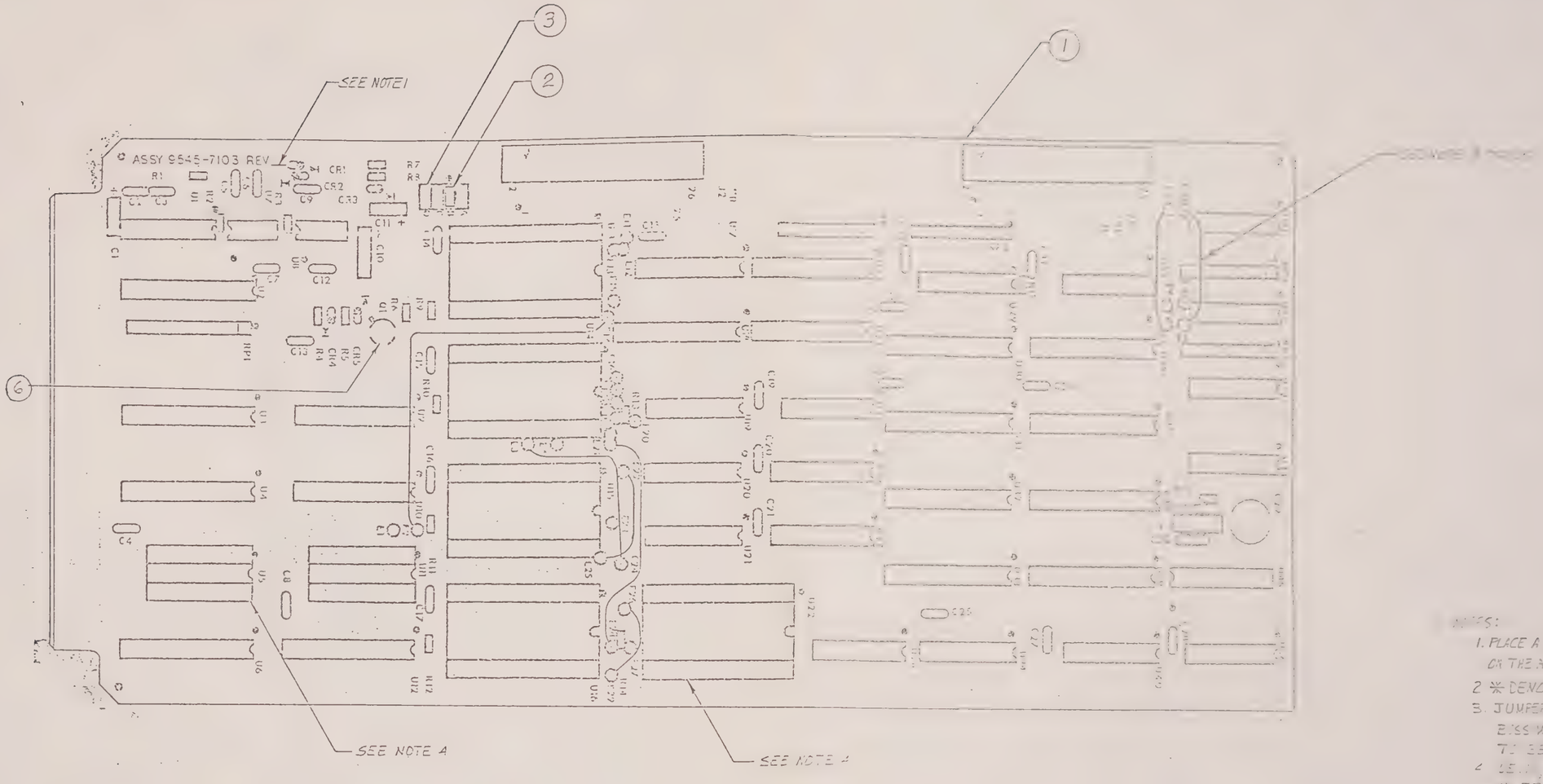
tau-tron inc
Chelmsford, Ma. 01824

DRAWING TITLE
STD/SPU INTERFACE
SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	N/A	57267	

SCALE N/A | SHEET 1

	H	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO # 02194 8-14-85 DA	8/27/85	



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. * DENOTES A STATIC SENSITIVE DEVICE.
3. JUMPERS 1 INCH OR LESS TO BE 24 AWG BUSS WIRE WITH SLEEVING. ALL OTHERS TO BE 30 AWG STRANDED WIRE
4. [illegible] 13 - 16, TO BE INSTALLED IN TEST.

D57158	S5200D RX	C.A.	2-2-81
NEXT ASSY	USED ON	BUDD	2-6-82
APPLICATION		K.B.	2-6-82

tau-tron

ASSEMBLY
STATUS & CONTROL PCB

SIZE	CODE IDENT NO	DRAWING NO	REV
D	954	57103	H

SCALE 2:1 | S5200D RX | SHEET 1 OF 1

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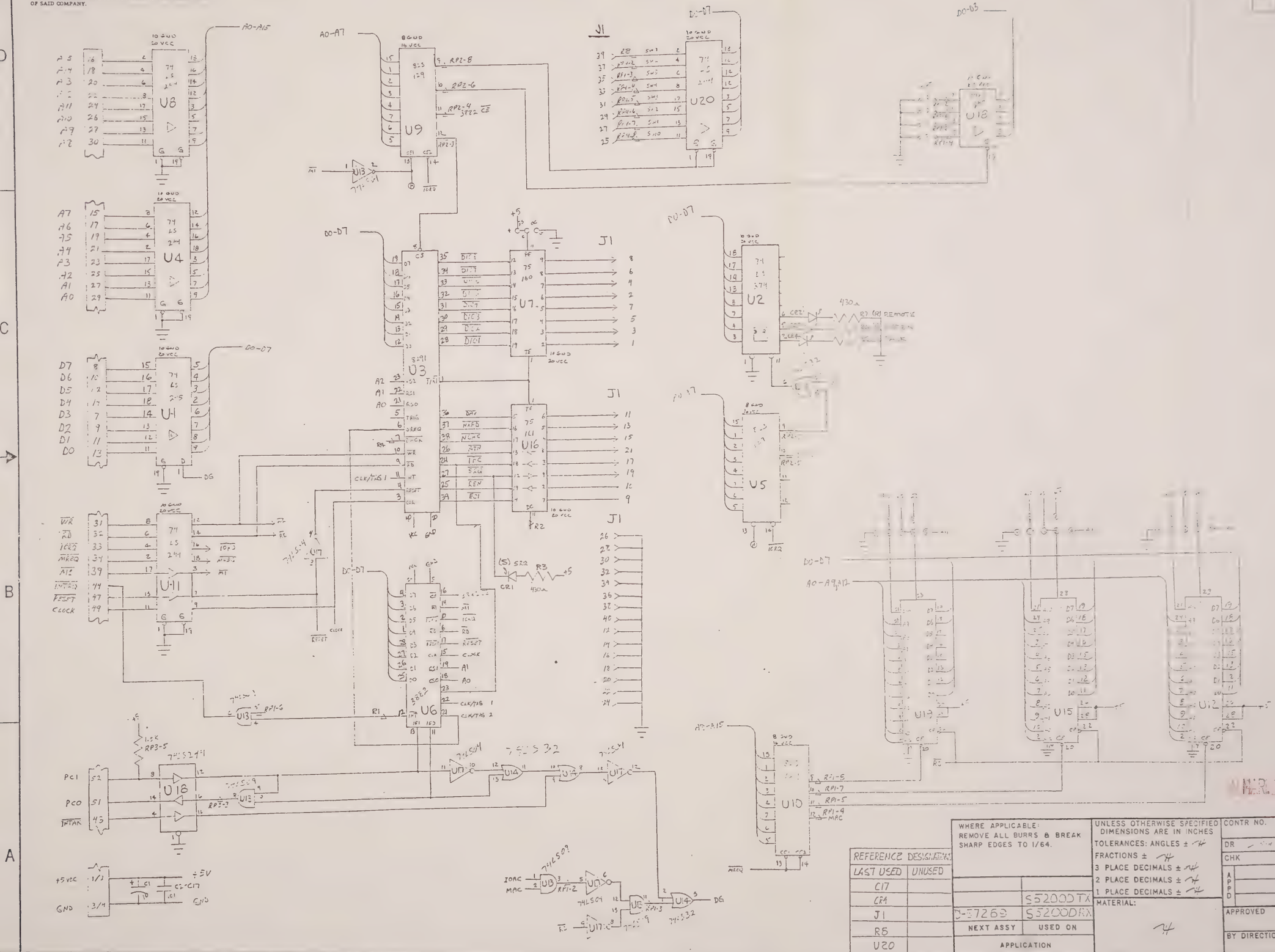
REVISIONS

		DESCRIPTION	DATE	APPROVED
		[illegible] TO [illegible] PER ECO 01085	1/18/82	BAC
	B	UPDATED PER DOC ACCURACY PROG PER ECO #02194 8-5-85 DH	8/27/85	PK



NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   A. RESISTANCE VALUES ARE IN OHMS, 1/8W, ±5%.
   B. CAPACITANCE VALUES ARE IN MICROFARADS.
2. ⏚ DENOTES 1.5K TO VCC (+5V).

MAR. 5. '87

REFERENCE DESIGNATIONS	
LAST USED	UNUSED
C17	
CR4	
J1	
R5	
U20	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

	S5200DTX
D-57269	S5200DRX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ⌗
FRACTIONS ± ⌗
3 PLACE DECIMALS ± ⌗
2 PLACE DECIMALS ± ⌗
1 PLACE DECIMALS ± ⌗
MATERIAL: ⌗

CONTR NO.
DR
CHK
APPD
APPROVED
BY DIRECTION OF

tau-tron inc
Chelmsford, Ma. 01824

DRAWING TITLE
STD/SB13 INTERFACE
SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.
D	⌗	57267

SCALE ⌗ SHEET 1

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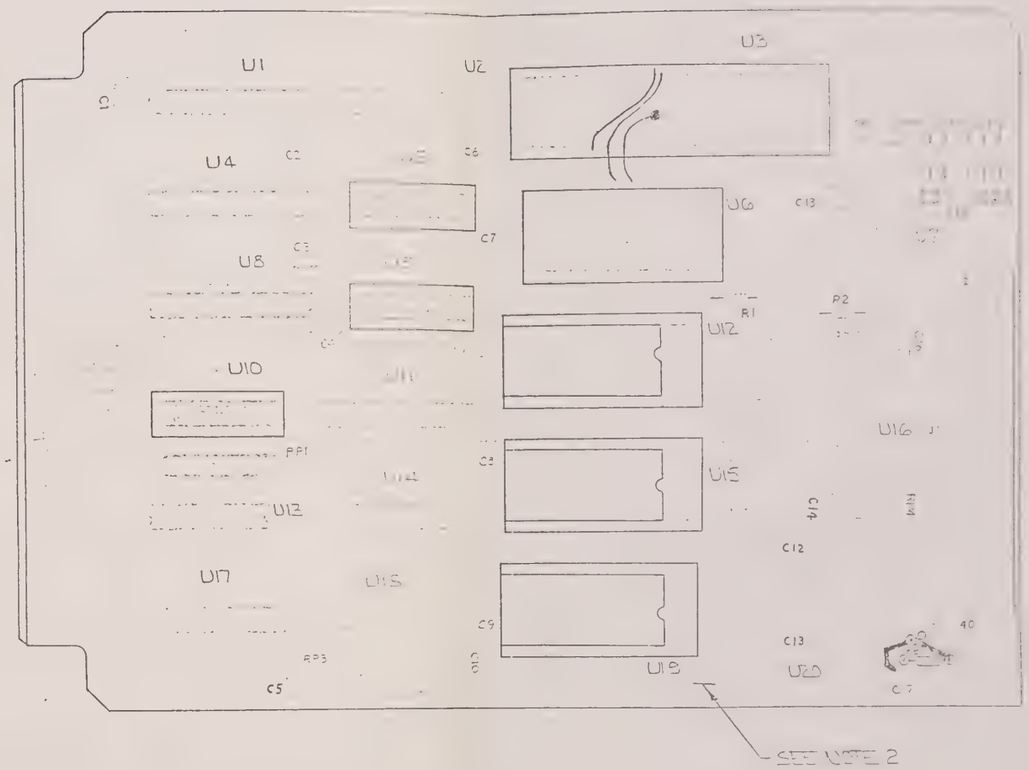
ZONE	LTR	DESCRIPTIONS	DATE	APPROVED
	A	REVISED TO MFG PER ECO [illegible]	1/8/82	[illegible]
	B	REVISED ECO [illegible]	5-7-82	[illegible]
	C	REV PER ECO # 1918		
	D	REV PER ECO # 1653 M.E.H 1/9/84	1-30-84	[illegible]
	E	UPDATED PER DOC ACCURACY PROG ECO # 02197	8/15/85	PK



NOTES:
1. PROMS WILL BE INSTALLED BY TEST.
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

MAR. 5. '[illegible]

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		DR [illegible] 11-19-81	STD/GPIB INTERFACE ASSEMBLY.
		CHK [illegible] 1-5-82	
		APPD [illegible]	
	S 5200 RX	MATERIAL:	SIZE D 954 57269
NEXT ASSY USED ON			
APPLICATION			SCALE 2/1

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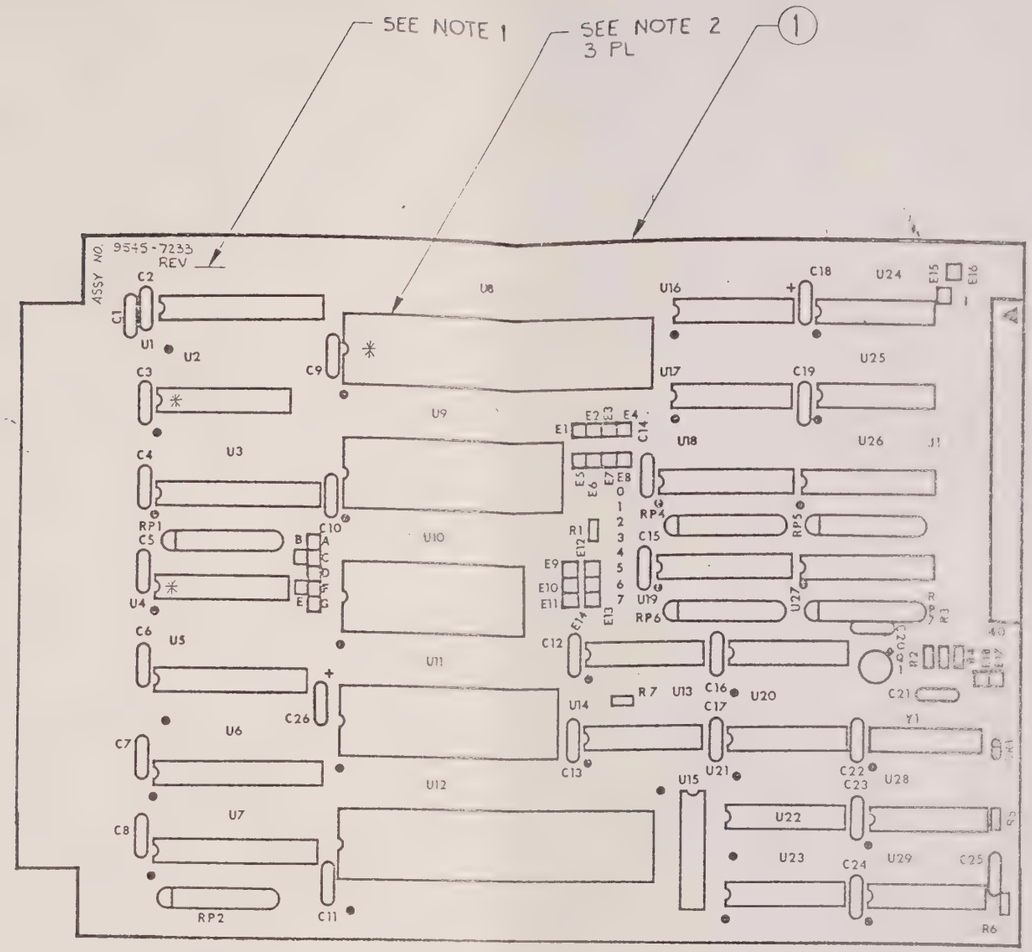
H	REDRAWN, WITH CHANGES PER DOC ACCURACY PROG ECO # 02197	85/07/22	8/15/85	
J	SHT. 2 NOW OBS. REV PER	ECO # 02703 MCD 1/12/87	1/3/87	
K	REV PER ECO # 02722	MCD 2/23/87		



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSY IN THE DESIGNATED AREA.
2. * DENOTES STATIC SENSITIVE DEVICE.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL	DR C. ATHERTON 4-9-81 CHK B COOPER 4-22-81 APPD K. BEANPRE 4-22-81	tau-tron
E-57158 NEXT ASSY / S5200D RX USED ON			CPU/RS32 ASSY
APPLICATION			D 57233
			2/1 S5200D RX 1 OF 1

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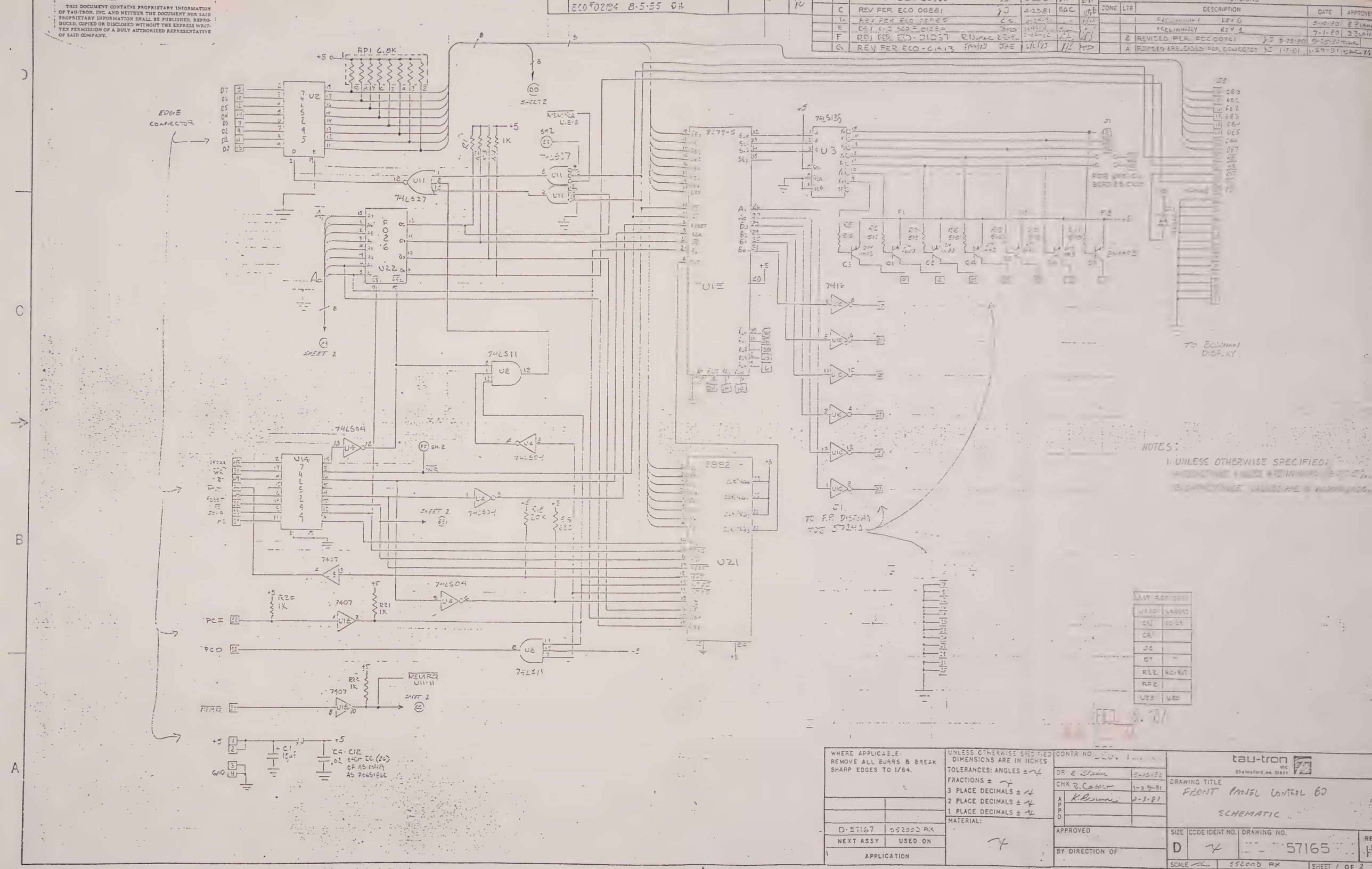
ECO #02194 8-5-55 DH

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	C	REV PER ECO 00861	4-23-81	BAC
	D	REV PER ECO 00965		
	E	CHG'D PER ECO # 01024		
	F	REV PER ECO-01037		
	G	REV PER ECO-01413	2/6/83	
		PRELIMINARY REV 0	5-10-80	BJ
		PRELIMINARY REV 1	7-1-80	BJ
	2	REVISED PER ECO 00741	9-25-80	
	A	RELEASED PER ECO 00707	1-29-81	



NOTES:
1. UNLESS OTHERWISE SPECIFIED:

PART REFERENCE	
U1-20	LAMPS
CN	
CR1	
J2	
C7	
R22	R21-R17
RP2	
U23	U20

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

D-57167	552000 RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 1°
FRACTIONS ± 1/4
3 PLACE DECIMALS ± .005
2 PLACE DECIMALS ± .01
1 PLACE DECIMALS ± .1
MATERIAL:

CONTR NO.
DR E. Chin 5-10-80
CHK B. Cooper 1-29-81
APPD K. Brown 2-3-81
APPROVED
BY DIRECTION OF

tau-tron INC Chelmsford, MA. 01824

DRAWING TITLE
FRONT PANEL CONTROL BD
SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	1	57165	H

SCALE 1/1 552000 RX SHEET 1 OF 2

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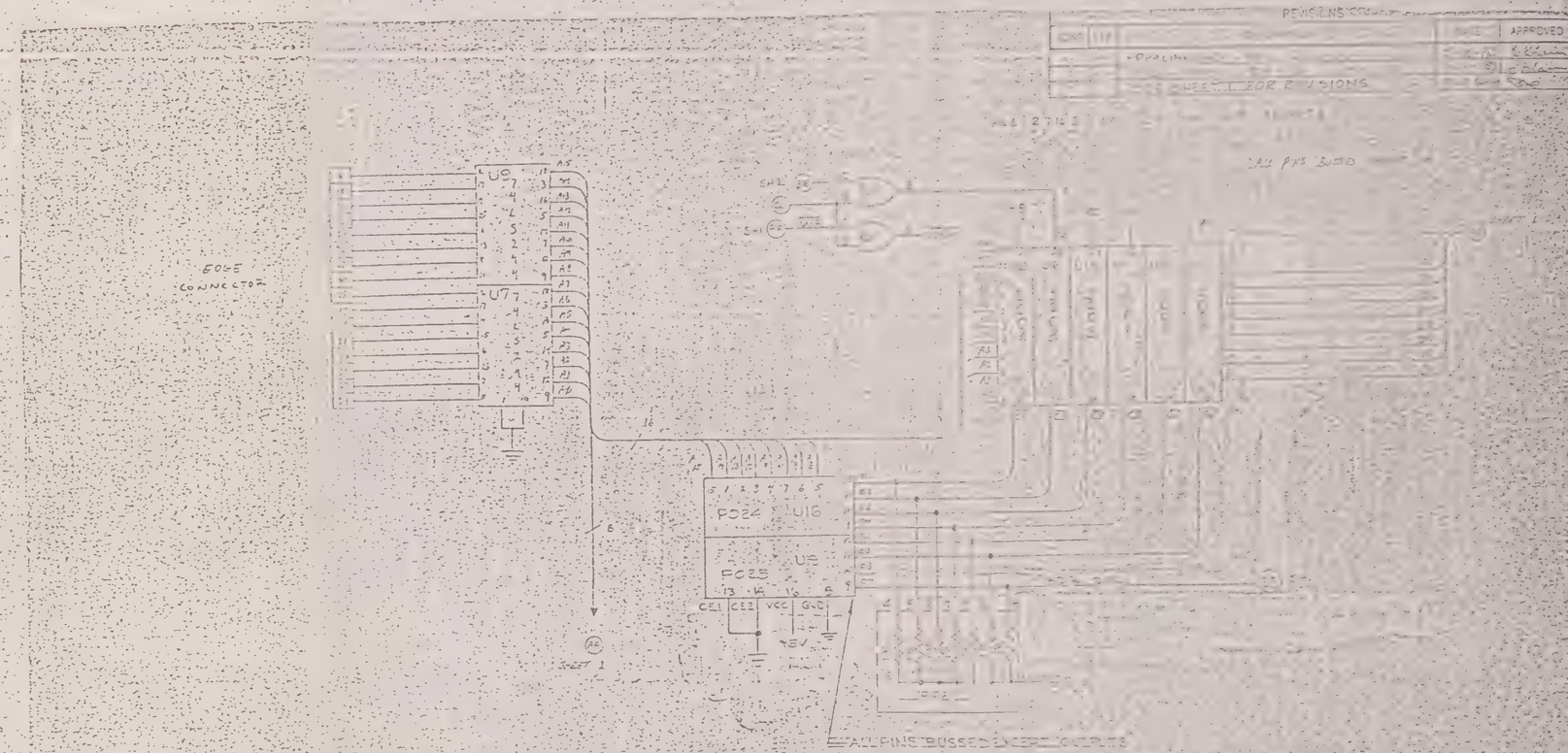

D57167	SS200D
NEXT ASSY	USED ON
APPLICATION	

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR. NO.
DR S. Blair 5-10-82
CHK
APPD
APPROVED
BY DIRECTION OF

tau-tron inc
Chelmsford, Mass. 01824

DRAWING TITLE
FRONT PANEL CONTROL BD
SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.
D		57165
SCALE	SS200D	SHEET 2 OF

07-0048 (8/71) .005 MYLAR

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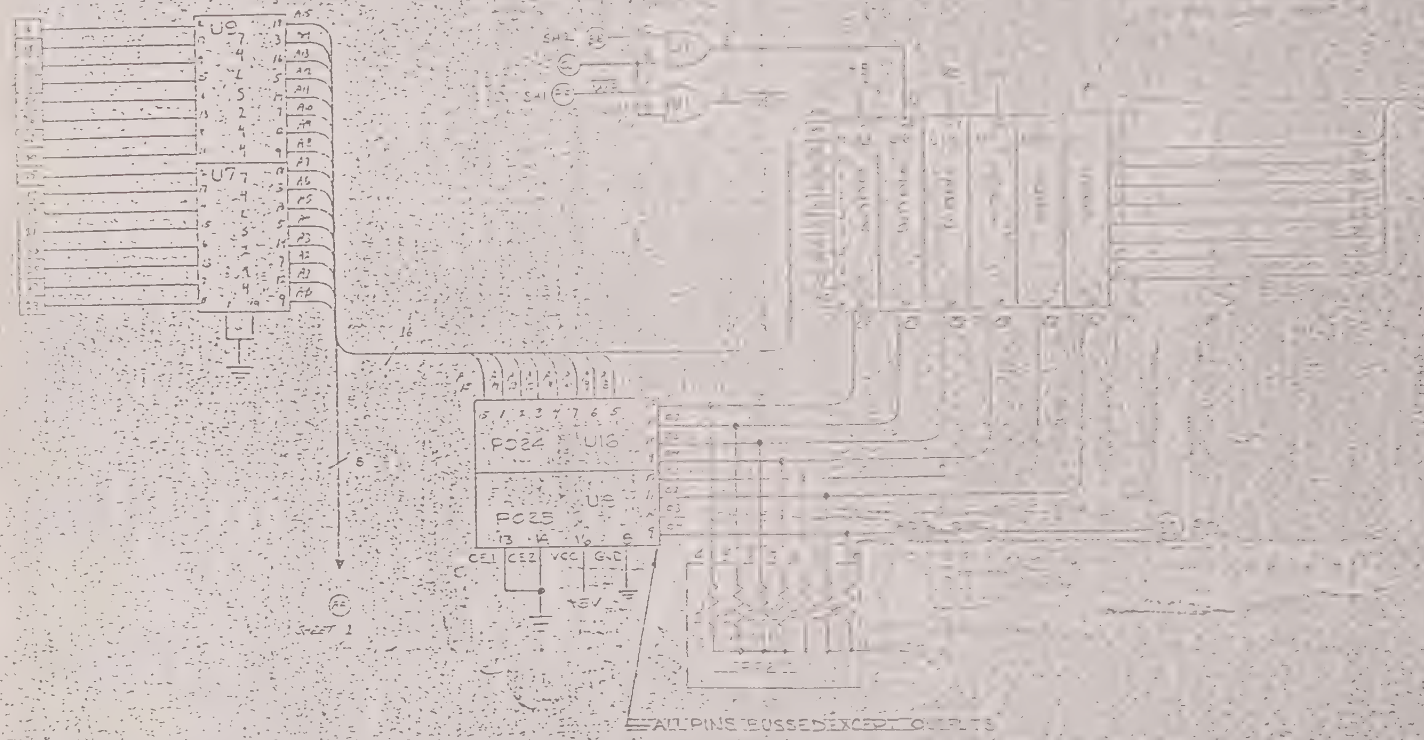
SEE SHEET 1 FOR REVISIONS



WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

D57167	55200D
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ±
FRACTIONS ±
3 PLACE DECIMALS ±
2 PLACE DECIMALS ±
1 PLACE DECIMALS ±
MATERIAL:

CONTR. NO.
DR E. [illegible] 5-10-80
CHK
APPD
APPROVED
BY DIRECTION OF

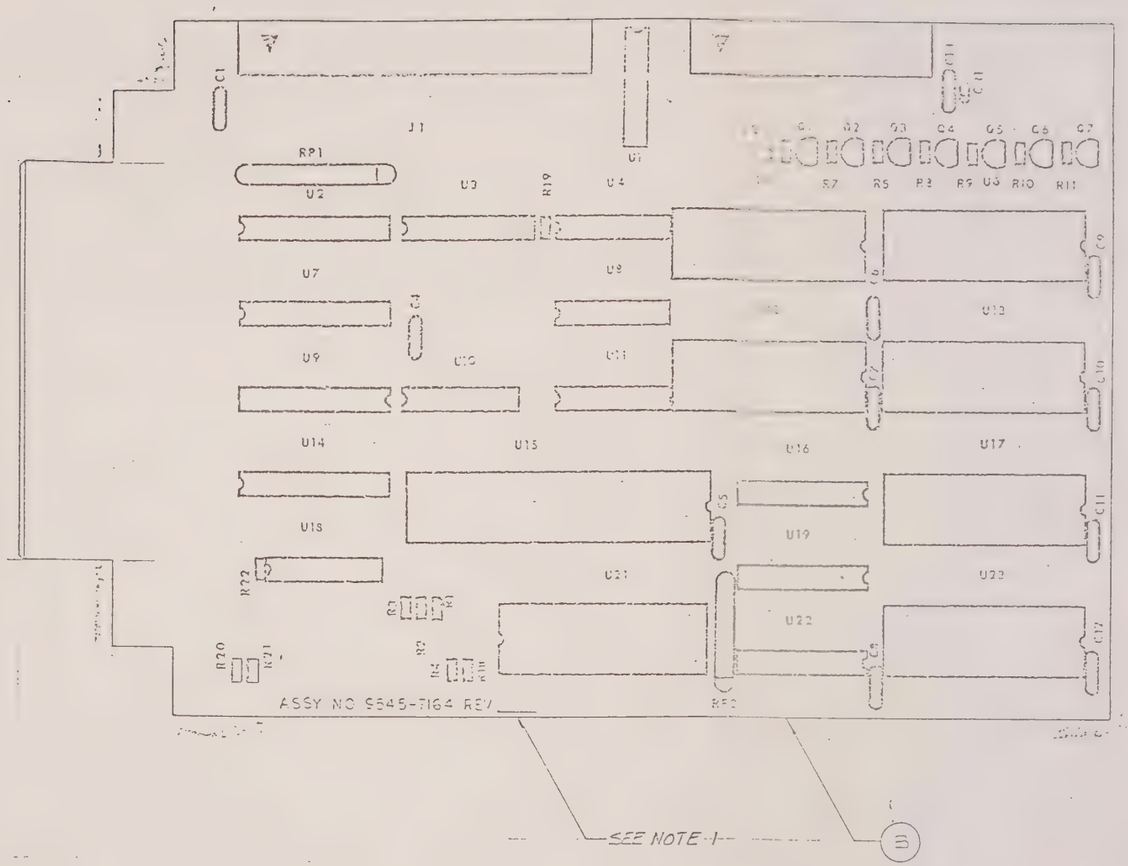
tau-tron inc. Chelmsford, Ma. 01824

DRAWING TITLE: FRONT PANEL CONTROL BD SCHEMATIC

SIZE	CODE IDENT NO.	DRAWING NO.
D		57165

SCALE — 55200D SHEET 2 OF

	L	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO #02194 8-6-85 DH	[illegible]	PK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 1 PLACE DECIMALS ± 2 PLACE DECIMALS ± 3 PLACE DECIMALS ±	DR J.T 1-6-81	teu-tron
		CHK B.C 1-23-81	DRAWING TITLE ASSEMBLY FRONT PANEL CONTROL PCB
		V.B 1-23-81	
D57061 / S5200D RX	MATERIAL ~	APPROVED	SIZE D / CODE IDENT NO 954 / DRAWING NO 57167 / REV L
NEXT ASSY / USED ON		BY DIRECTION OF	SCALE ~ / SHEET 1 OF 1
APPLICATION			

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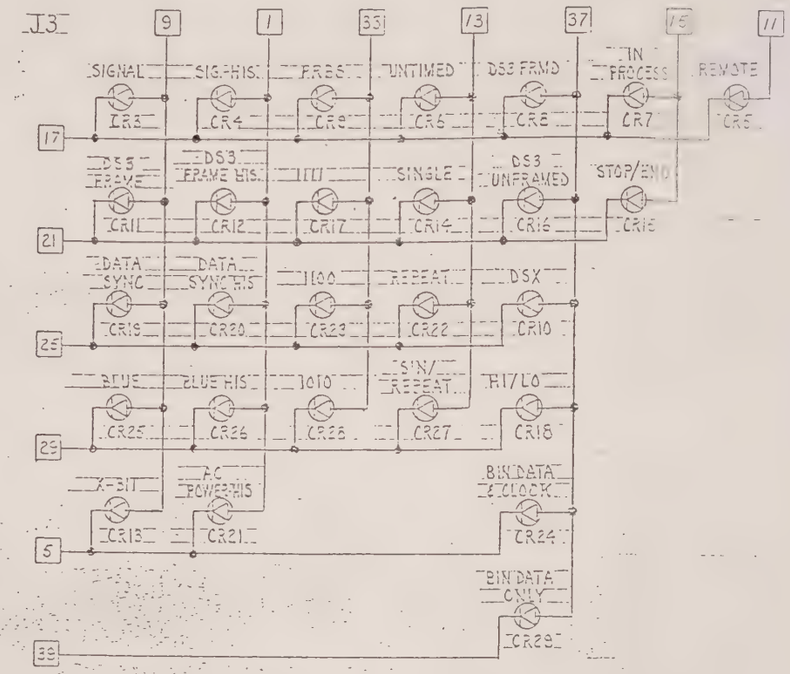
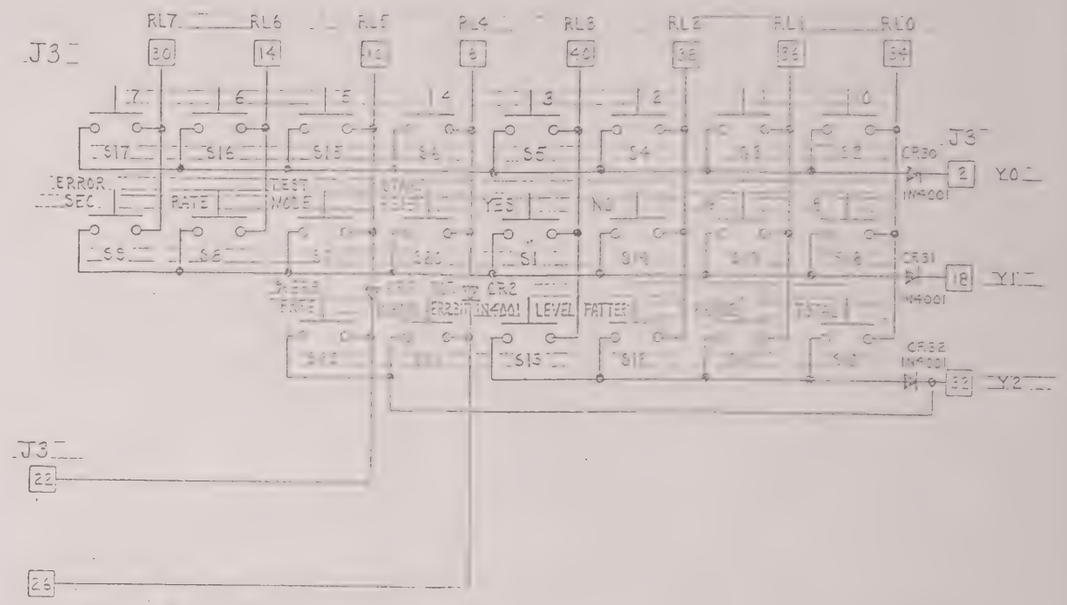
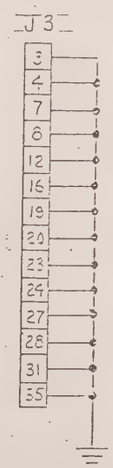
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	0	ORIGINATED C.S. 5-12-80	5-22-80	B Elsin
	1	REV PER PDC00742 C.S. 8-23-80	11-24-80	B. Cooper
	A	UPDATED PER DOC ACCURACY PROG PER ECO #02194 8-5-85 DJL	8/27/85	PK







LAST REF DES. USED	UNUSED
S22	
CR32	

REFERENCE DRAWINGS:
ASSEMBLY: D57143
FABRICATION: D57142

WHERE APPLICABLE:
REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

	S5200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

CONTR NO.

DR	C. Smith	5-12-80
CHK	✓	5-22-80
APPD	B Elsin	5-22-80
APPROVED		
BY DIRECTION OF		

tau-tron INC
Chelmsford, Ma. 01824

DRAWING TITLE
SCHEMATIC
FRONT PANEL DISPLAY BOARD

SIZE	CODE IDENT NO.	DRAWING NO.	REV
C	~	57141	A

SCALE — | S5200D | SHEET 1 OF 1

07-0043 (6/71) .003 MYLAR

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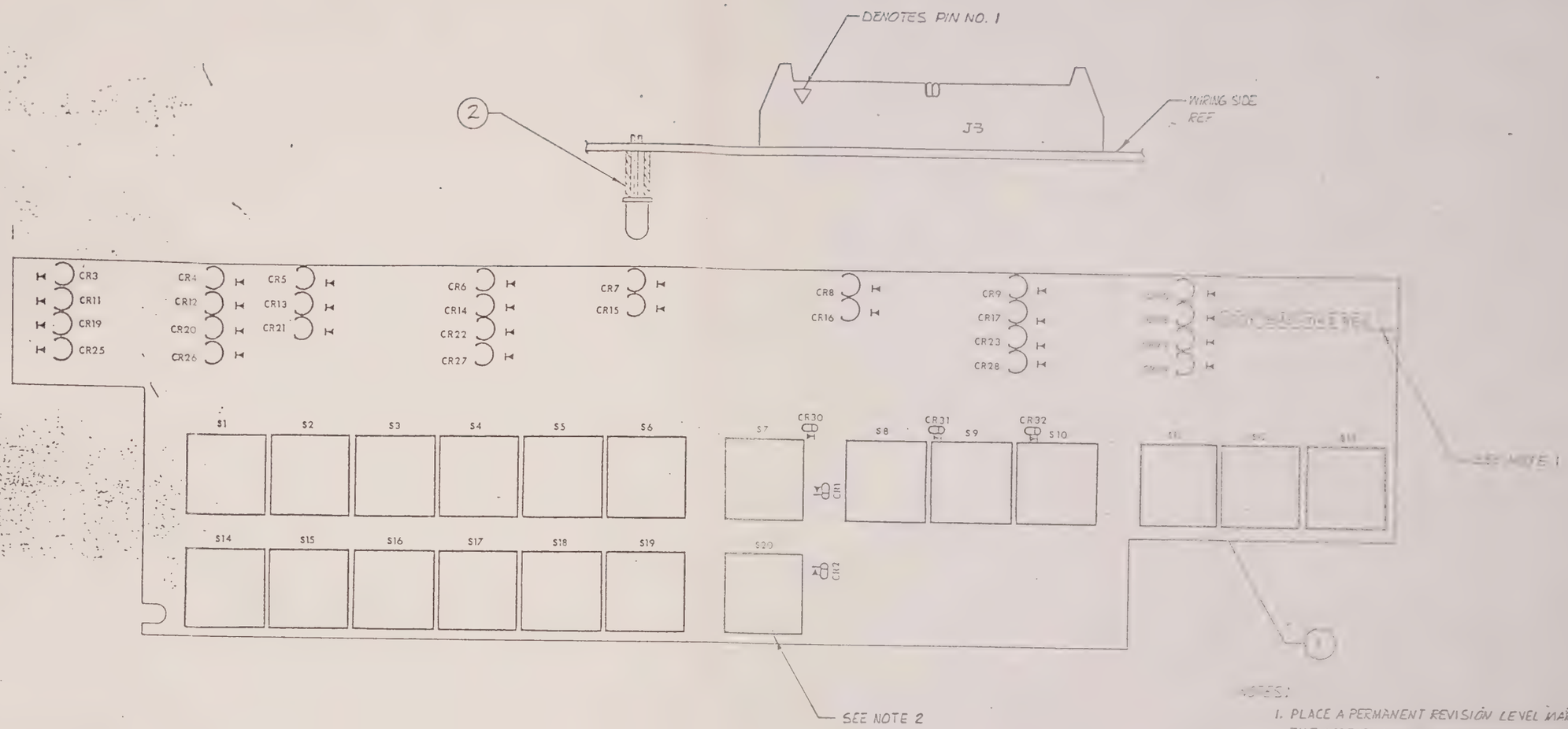
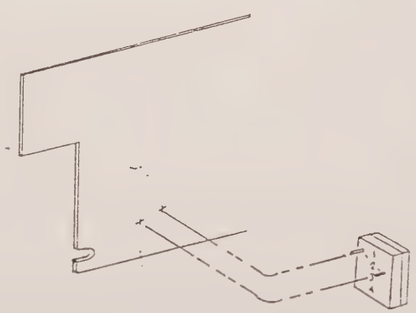
	D	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO# 02194 8-12-85 DH	8/27/85	PK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. INSTALL SWITCHES AFTER WAVE SOLDERING, CLEAN WITH ISOPROPYL ALCOHOL ONLY.



INSTALL ALL SWITCHES, WITH NUMBERS STAMPED ON REAR, UP, AS SHOWN.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64

D57153	SS2000 RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ANGLES ± —
FRACTIONS ± —
3 PLACE DECIMALS ± —
2 PLACE DECIMALS ± —
1 PLACE DECIMALS ± —
MATERIAL: —

CONTR NO

DR	J. P	5-5-80
CHK	T. S.	5-8-80
APPD	E.B.	5-8-80

APPROVED
BY DIRECTION OF

tau-tron INC Chelmsford, MA 01824

DRAWING TITLE: FRONT PANEL DISPLAY BOARD ASSEMBLY

SIZE	CODE IDENT NO	DRAWING NO	REV
D	954	57143	D

SCALE 2/1 | SS2000 RX | SHEET 1 OF 1

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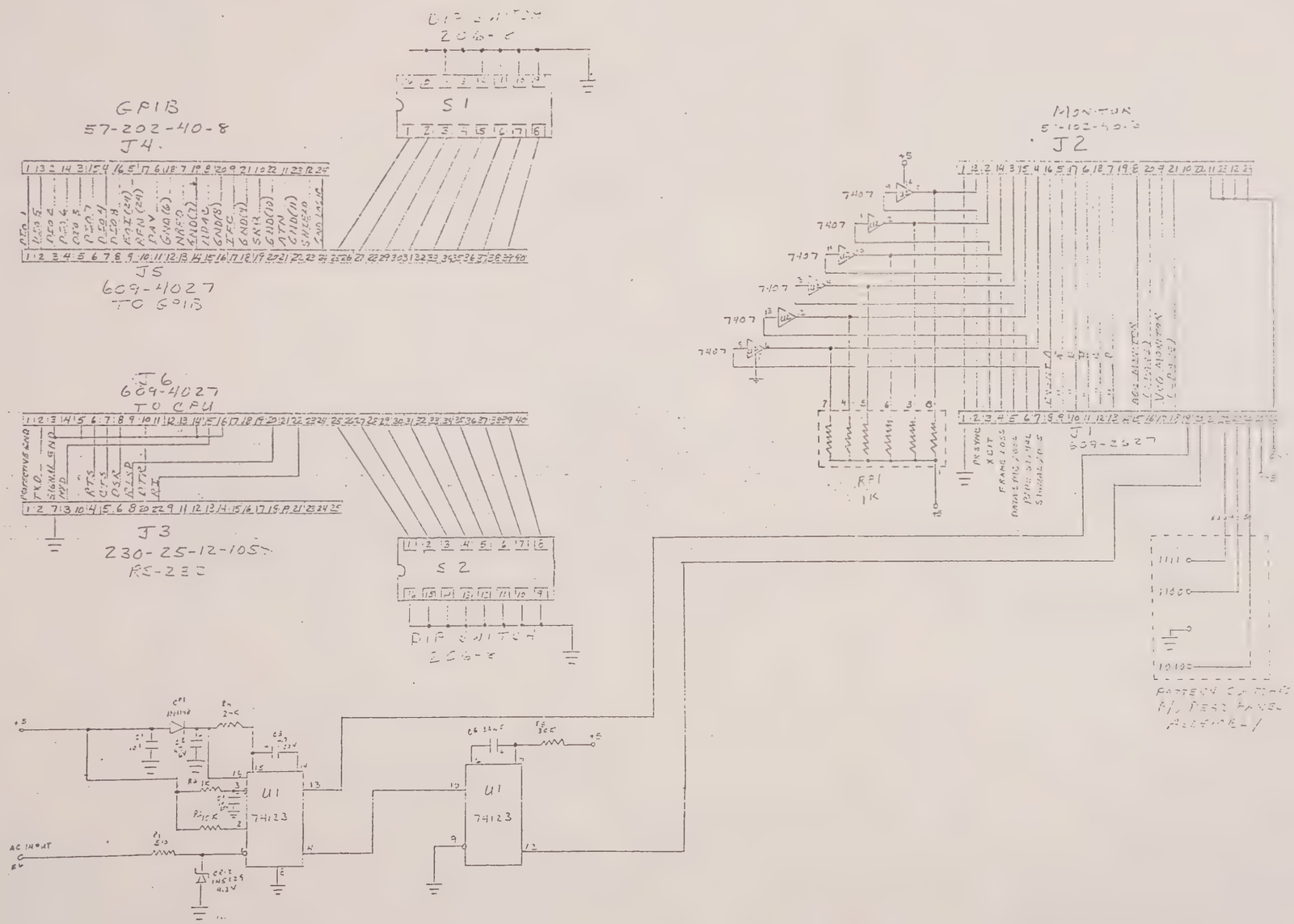
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	RELEASED TO MFG. PER ECO 01086	[illegible]	[illegible]
	B	REV PER ECO-01120 KN June 4-19-82	[illegible]	[illegible]
	C	UPDATED PER DOC ACCURACY PROG PER ECO# 02194 8-5-85 DB	[illegible]	[illegible]



NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. RESISTOR VALUES ARE IN OHMS, 1/4W, ±5%.
B. CAPACITOR VALUES ARE IN MICROFARADS.

REFERENCE DESIGNATIONS	
LAST USED	LAST USED
C5	U2
CR2	
J6	
R5	
RP1	
S2	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.

C57274	55200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± ~
FRACTIONS ± ~
3 PLACE DECIMALS ± ~
2 PLACE DECIMALS ± ~
1 PLACE DECIMALS ± ~
MATERIAL: ~

CONTR NO.
DR K. Crespo
CHK RL 11-4-81
APPD K. Crespo 11-4-81
APPROVED
BY DIRECTION OF

tau-tron inc
Chelmsford, ma. 01824

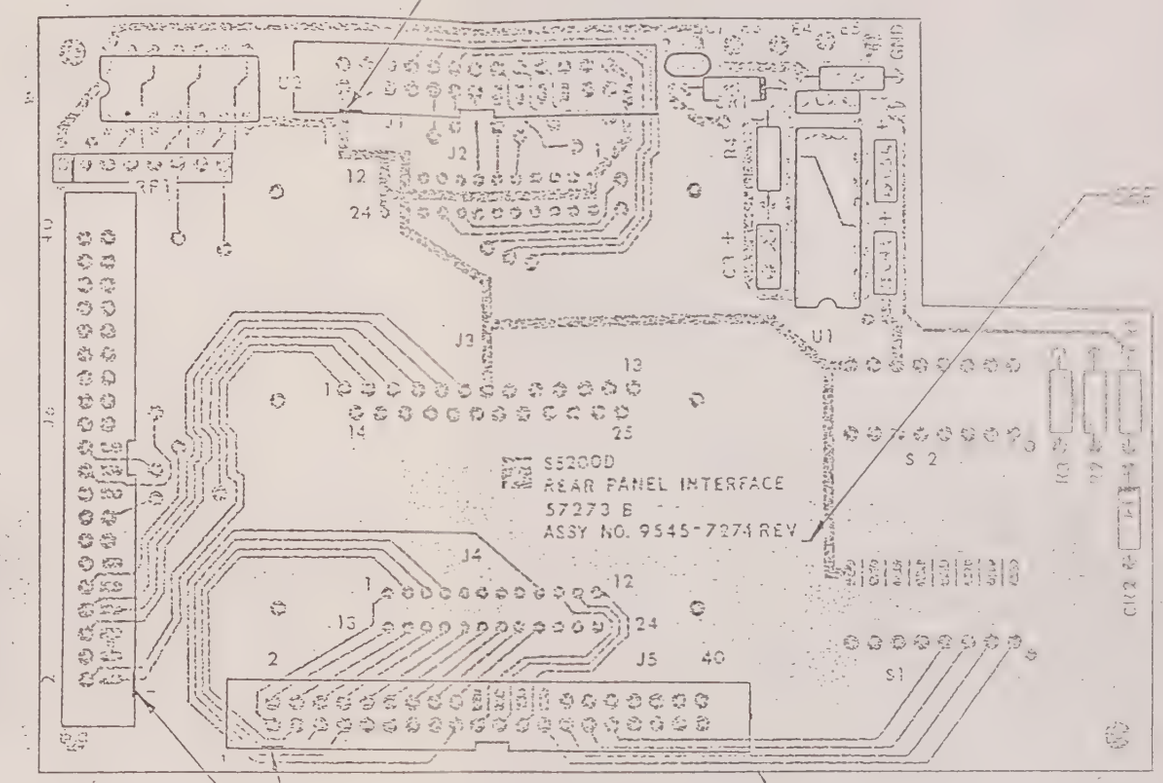
DRAWING TITLE
SCHEMATIC
REAR PANEL INTERFACE P.C.B.

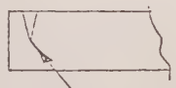
SIZE	CODE IDENT NO.	DRAWING NO.	REV
D	~	57272	C

SCALE ~ | 55200D RX | SHEET 1 OF 1

NOTICE OF
CONFIDENTIAL INFORMATION

F	UPDATED PER DOC ACCURACY PROG PER ECO #02194 7-18-85		





ARROW DENOTES PIN 1 ON CONNECTOR.

DETAIL "A"

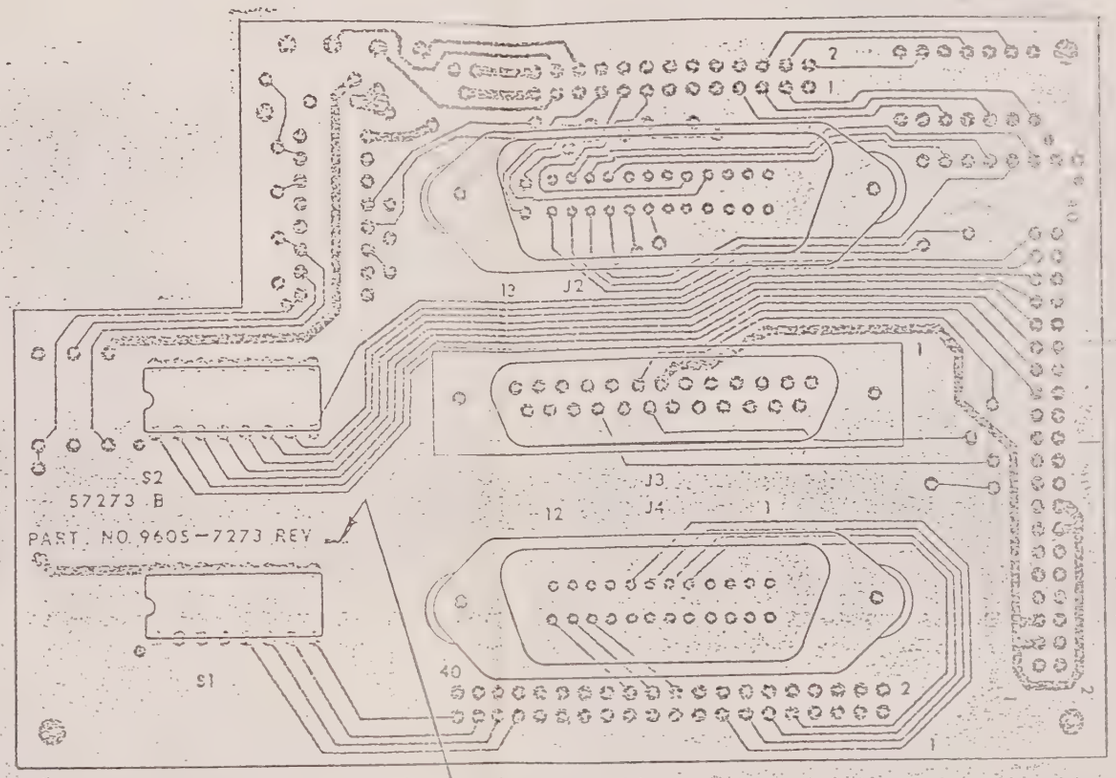
NOTES:

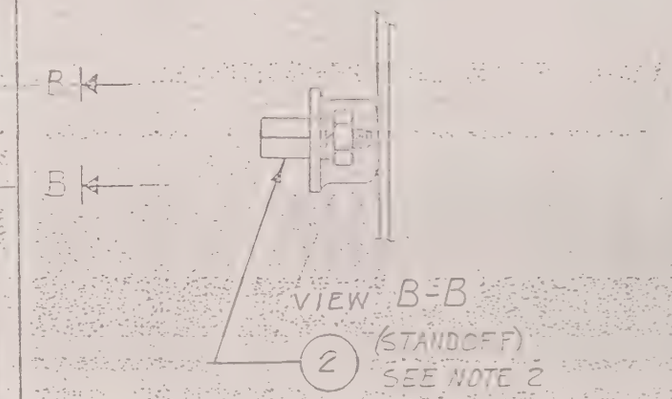
1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. ASSEMBLE STANDOFFS TO CONNECTOR J3 PRIOR TO SOLDERING TO BOARD. USE HARDWARE SUPPLIED WITH STANDOFF.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± 1° FRACTIONS ± 1/64 3 PLACE DECIMALS ± .005 2 PLACE DECIMALS ± .01 1 PLACE DECIMALS ± .1	CONTR NO	AUG. 12 '87 tau-tron
		DR C.S. 11-3-81	DRAWING TITLE ASSEMBLY - REAR PANEL INTERFACE PCB (GPIB OPT)
		CHK R.L. 11-4-81	
		APPD K.B. 11-4-81	
S5200D RX	MATERIAL	APPROVED	SIZE C · CODE IDENT NO 954 · DRAWING NO. 57274 · REV F
NEXT ASSY · USED ON		BY DIRECTION OF	SCALE 2/1 · S5200D RX · SHEET 1 OF 2
APPLICATION			

NOTICE OF CONFIDENTIAL INFORMATION

	F	SEE SHEET 1 FOR REVISIONS



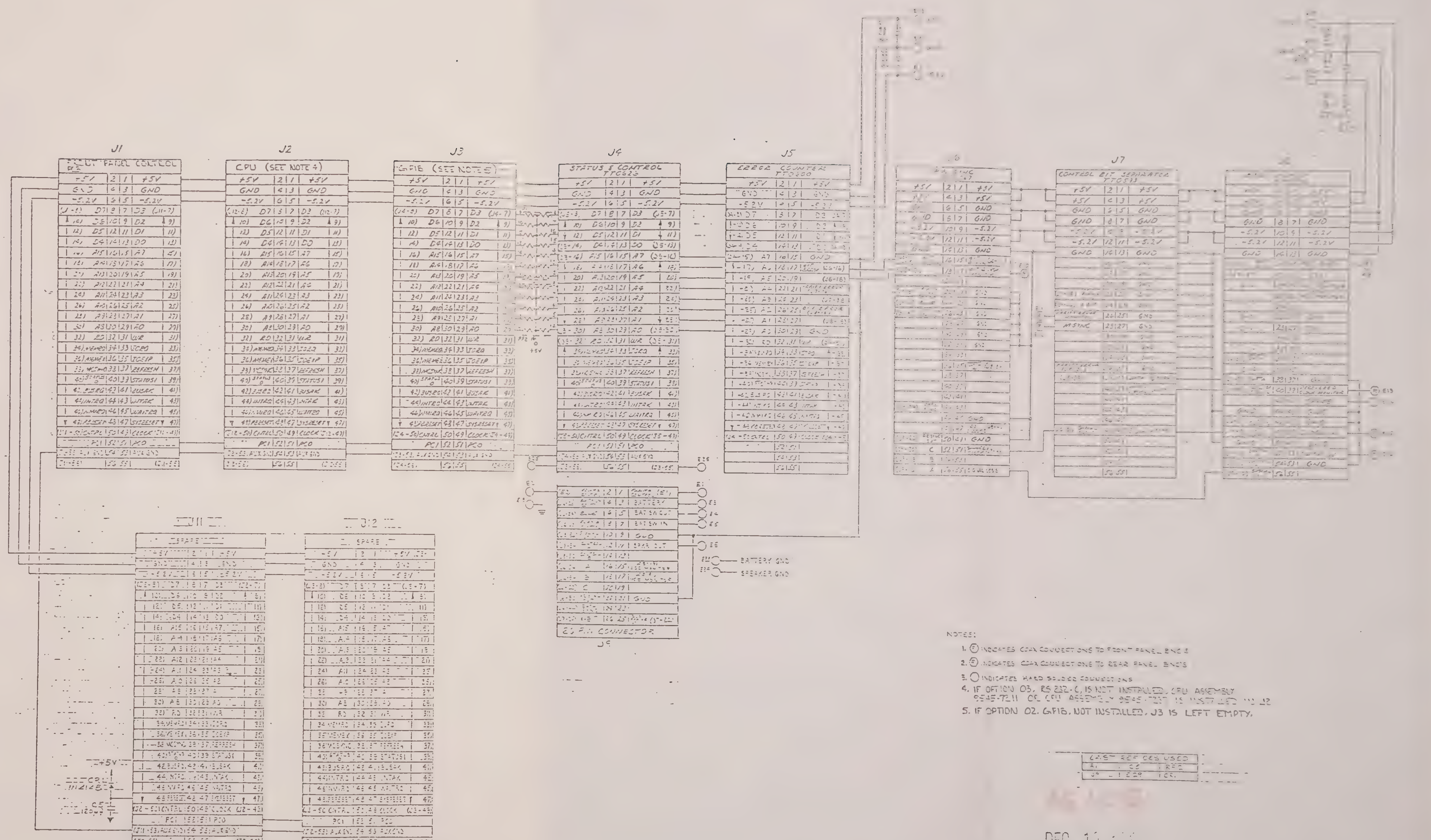


WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	CONTR NO.	tau-tron
	TOLERANCES: ANGLES ± ~	DR C. S. 11-3-81	DRAWING TITLE
	FRACTIONS ± ~	CHK R.L. 11-4-81	DEV. 1 ASSEMBLY - REAR PANEL
	3 PLACE DECIMALS ± ~	APPD K.B. 11-4-81	INTERFACE PCB (GPIB OPT)
	2 PLACE DECIMALS ± ~		
	1 PLACE DECIMALS ± ~		
	MATERIAL: ~	APPROVED	SIZE C · CODE IDENT NO 954 · DRAWING NO. 57274 · REV F
NEXT ASSY: S5200D RX · USED ON		BY DIRECTION OF	SCALE 2/1 · S5200D RX · SHEET 2 OF 2
APPLICATION			

PROPRIETARY NOTICE

REV LTR	DESCRIPTION	DATE	APPROVED
C	REVISED PER DOC ACCURACY PROG PER ECO #C2194 8-5-85 DK	8/21/85	

COMPONENT SIDE



NOTES:

1. Ⓕ INDICATES COAX CONNECTIONS TO FRONT PANEL BNC'S
2. Ⓡ INDICATES COAX CONNECTIONS TO REAR PANEL BNC'S
3. ◯ INDICATES HARD SOLDERED CONNECTIONS
4. IF OPTION 03, RS 232-C, IS NOT INSTALLED, CPU ASSEMBLY 9545-7211 OR CPU ASSEMBLY 9545-7237 IS INSTALLED IN J2
5. IF OPTION 02, GPIB, NOT INSTALLED, J3 IS LEFT EMPTY.

LAST REF DES USED		
R1	C5	REG
J9	E29	CR1

DEC. 1

FRONT VIEW

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± ~ FRACTIONS ± ~ 3 PLACE DECIMALS ± ~ 2 PLACE DECIMALS ± ~ 1 PLACE DECIMALS ± ~	CONTR NO.	tau-tron
55200DRX	MATERIAL: ~	DR / CHK / APPD	DRAWING TITLE: SCHEMATIC, RECEIVER MOTHER BOARD PCB
NEXT ASSY / USED ON		APPROVED / BY DIRECTION OF	SIZE E / CODE IDENT NO. ~ / DRAWING NO. 57059 / REV C
APPLICATION			SCALE NONE / 55200DRX / SHEET 1 OF 1

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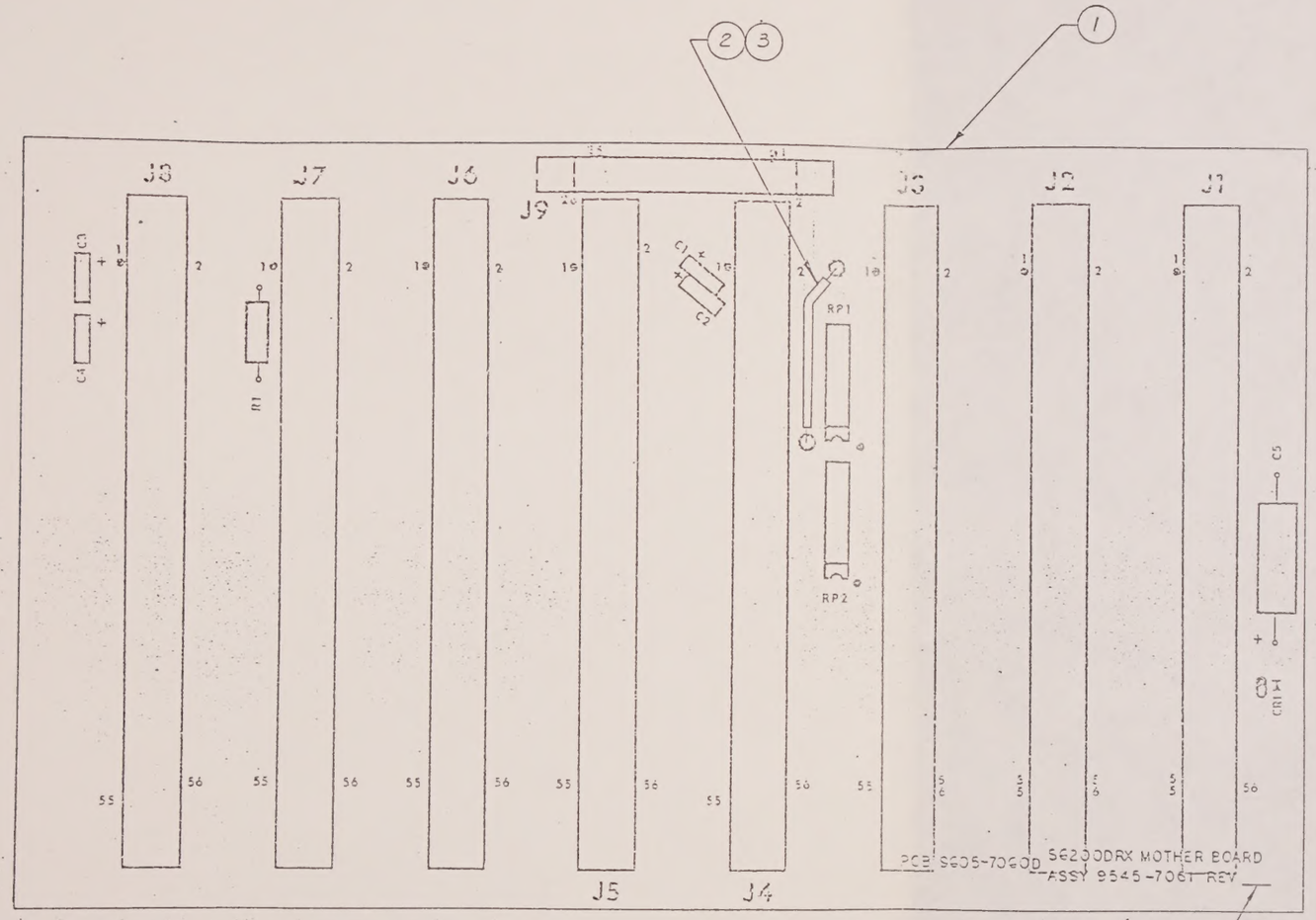
			DATE	APPROVED
	D	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO 02194 7-25-85 DH	7/28/85	PK JC



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

AUG. 12. '87

DEC. 12. '86

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ±	CONTR NO	tau-tron
		DR C.S. 10-20-80	DRAWING TITLE
		CHK B.C. 10-24-80	ASSEMBLY, MOTHER BOARD
		APPD K.B. 10-24-80	
		APPROVED	
S5200DRX	MATERIAL:	BY DIRECTION OF	SIZE D · CODE IDENT NO 954 · DRAWING NO. 57061 · REV D
NEXT ASSY · USED ON			SCALE 2/1 · S5200D RX · SHEET 1 OF 1
APPLICATION			

57061

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